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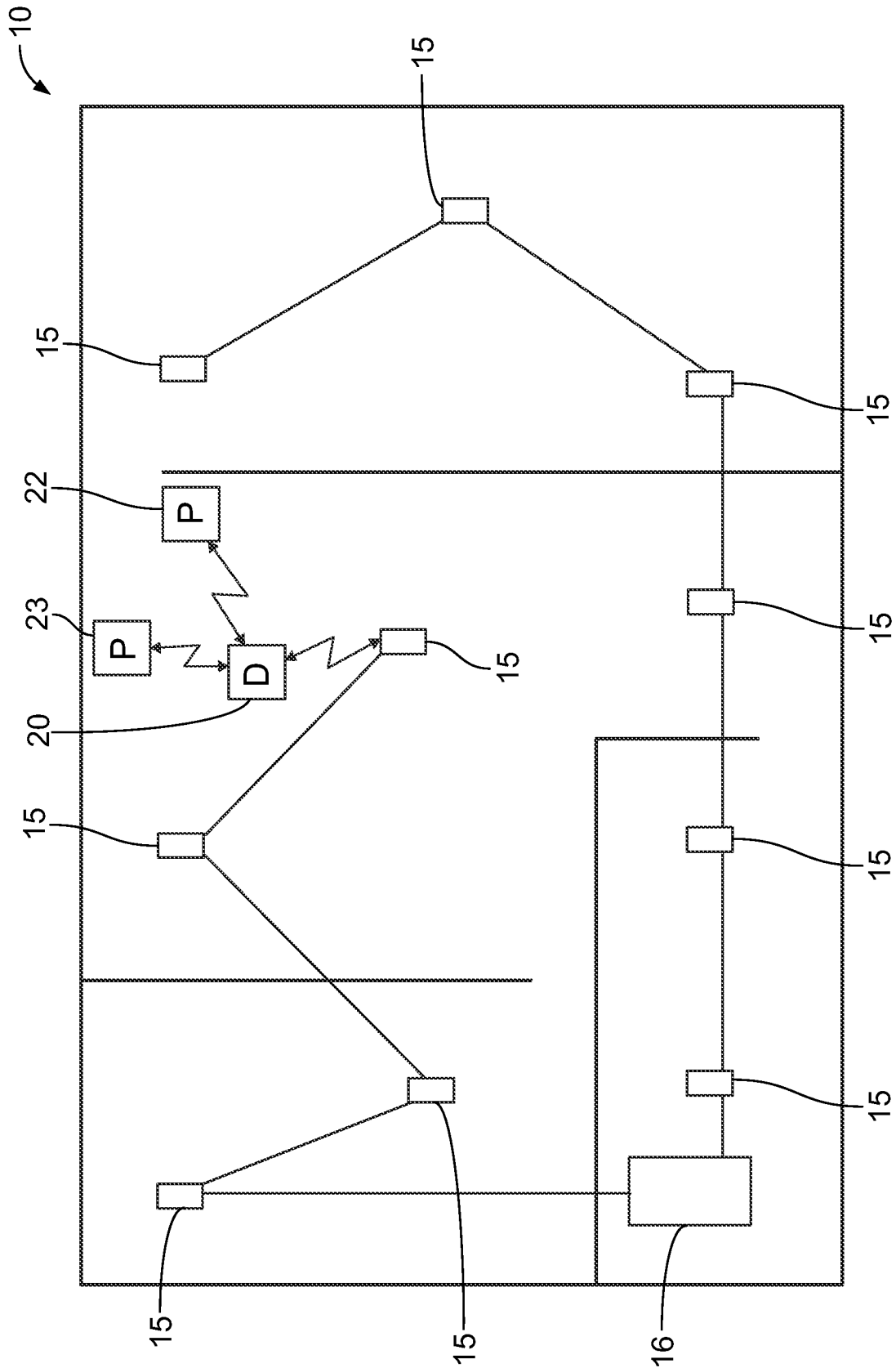


FIG. 1A

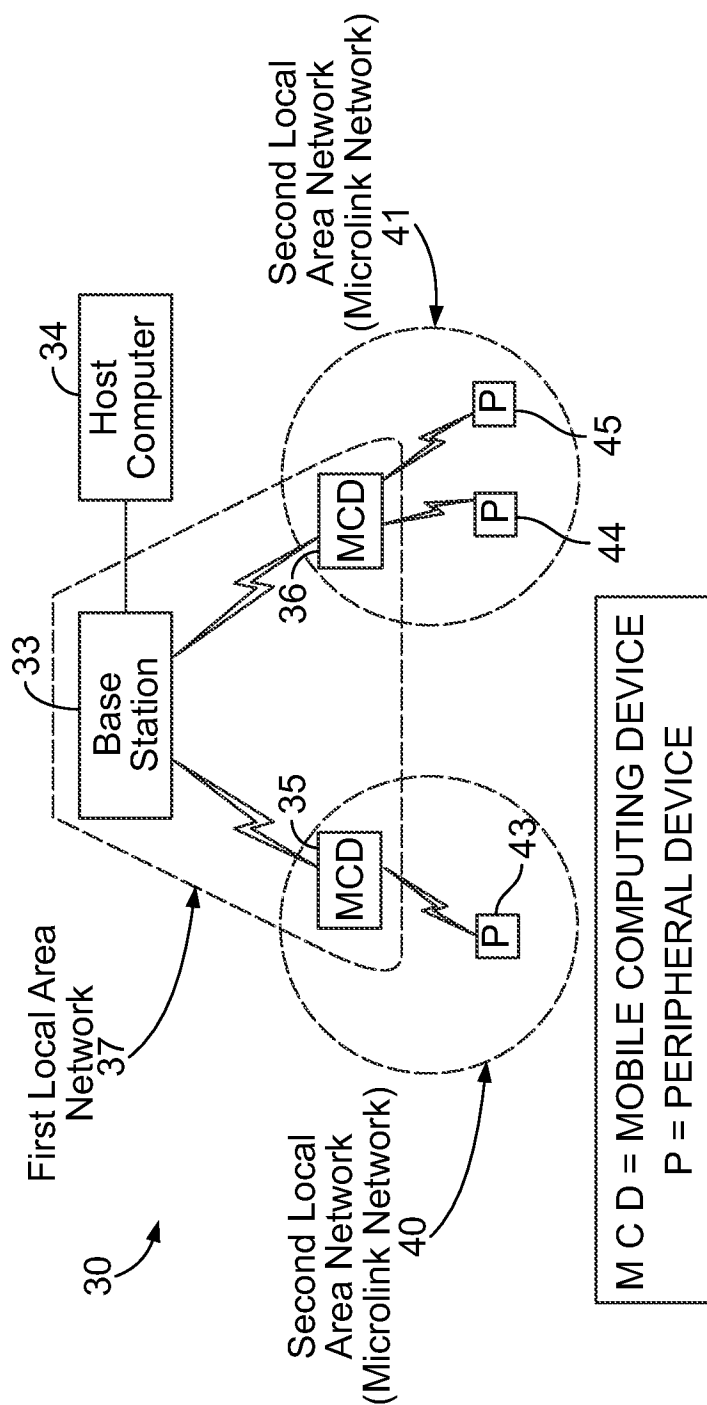


FIG. 1B

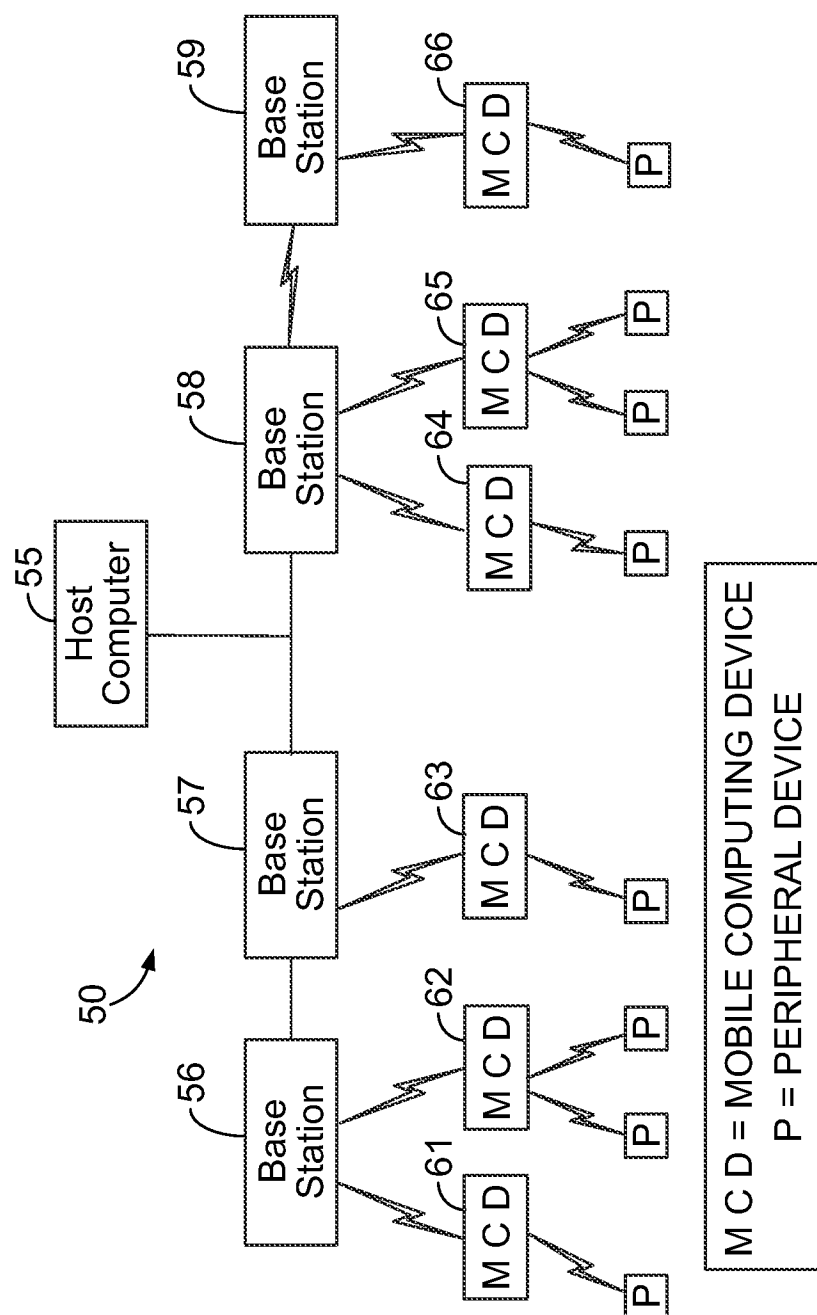


FIG. 1C

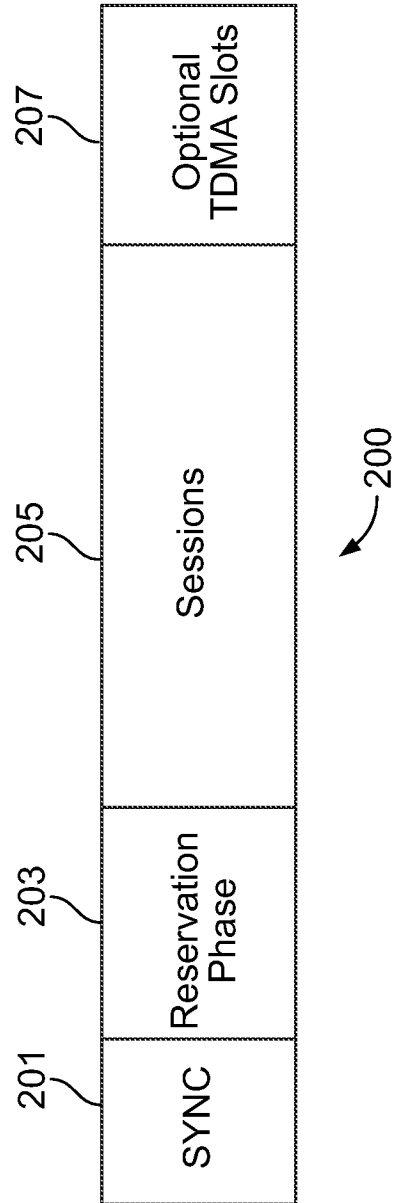


FIG. 2

10 ms	10 ms	10 ms	10 ms	10 ms	10 ms	10 ms
Freq. 1 2466 MHz	Freq. 2 2411 MHz	Freq. 3 2417 MHz	Freq. 4 2472 MHz	Freq. 5 2444 MHz	Freq. 79 2403 MHz
301	301	301	301	301	301	301

FIG. 3A

20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 ms
Freq. 1 2466 MHz	Freq. 2 2411 MHz	Freq. 3 2417 MHz	Freq. 4 2472 MHz	Freq. 5 2444 MHz	Freq. 79 2403 MHz
303	303	303	303	303	303	303

FIG. 3B

Freq. 1: 2466 MHz		Freq. 2: 2411 MHz		Freq. 3: 2417 MHz		Freq. 5: 2444 MHz	
Al. 1 20 ms	Al. 2 20 ms	Al. 1 20 ms	Al. 2 20 ms	Al. 1 20 ms	Al. 2 20 ms	Al. 1 20 ms	Al. 2 20 ms

401

FIG. 4A

Freq. 1: 2466 MHz				Freq. 2: 2411 MHz			
Al. 1 20 ms	Al. 2 20 ms	Al. 3 20 ms	Al. 4 20 ms	Al. 1 20 ms	Al. 2 20 ms	Al. 3 20 ms	Al. 4 20 ms

401

FIG. 4B

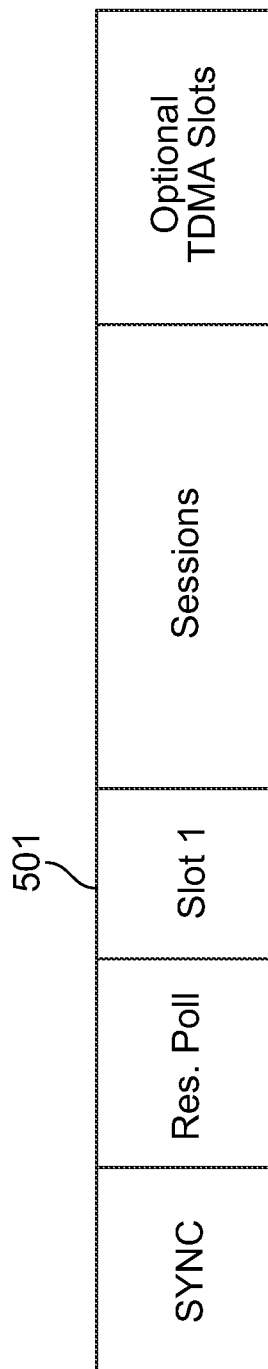


FIG. 5A

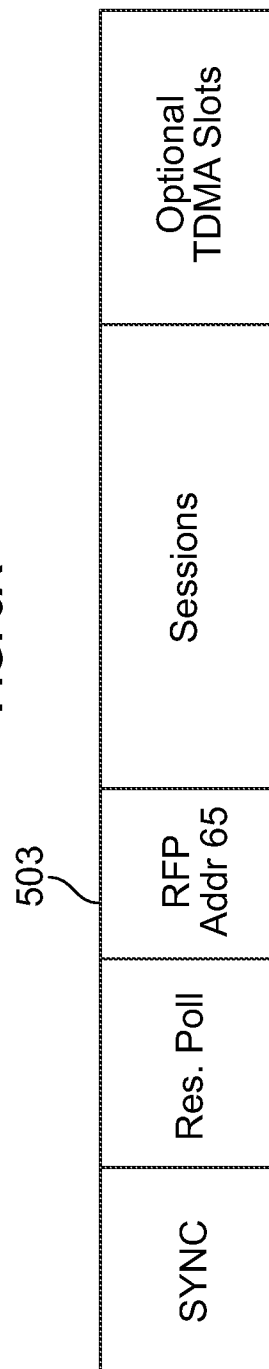


FIG. 5B

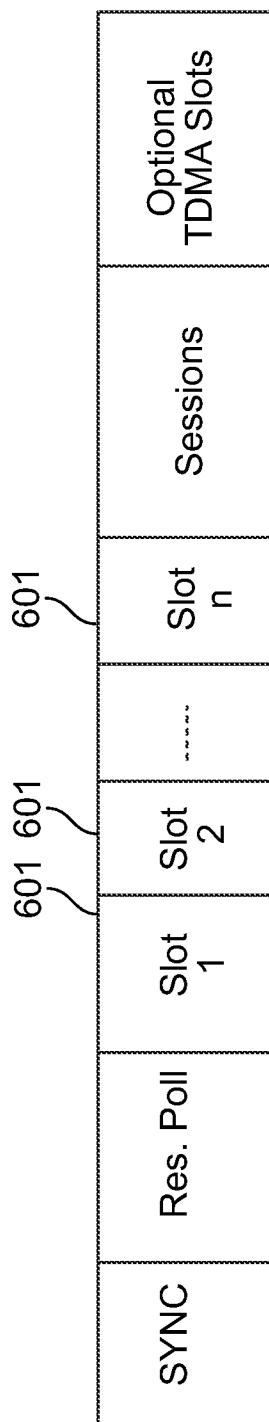


FIG. 6A

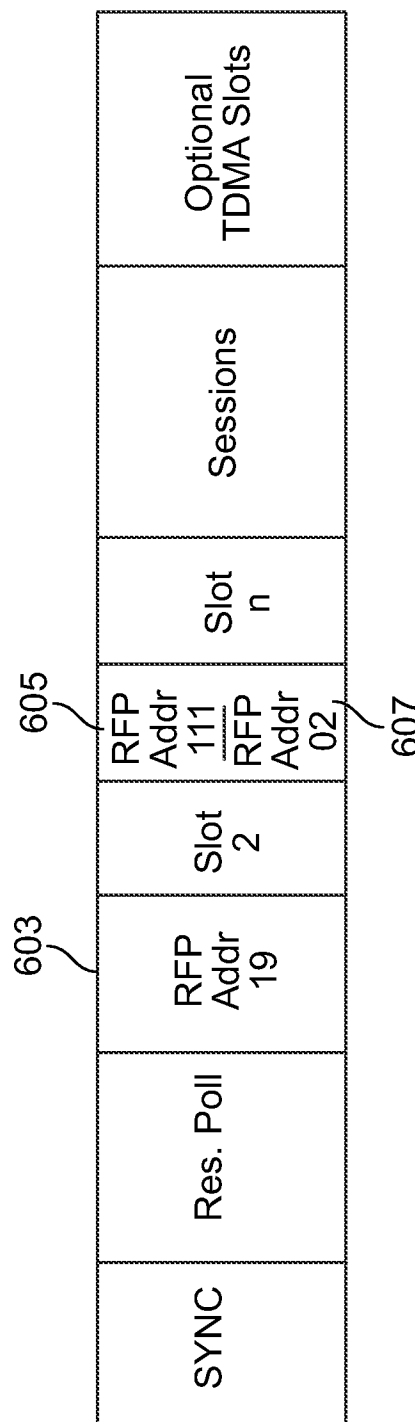


FIG. 6B

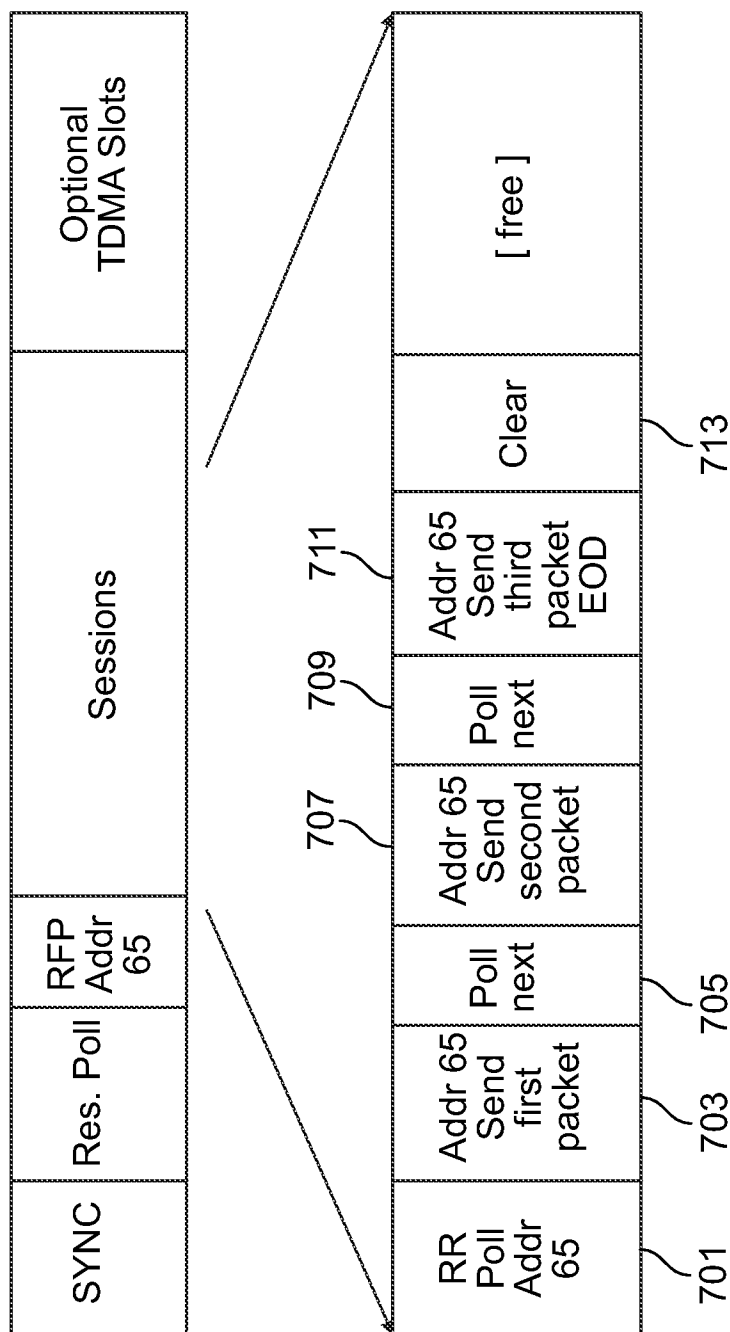


FIG. 7A

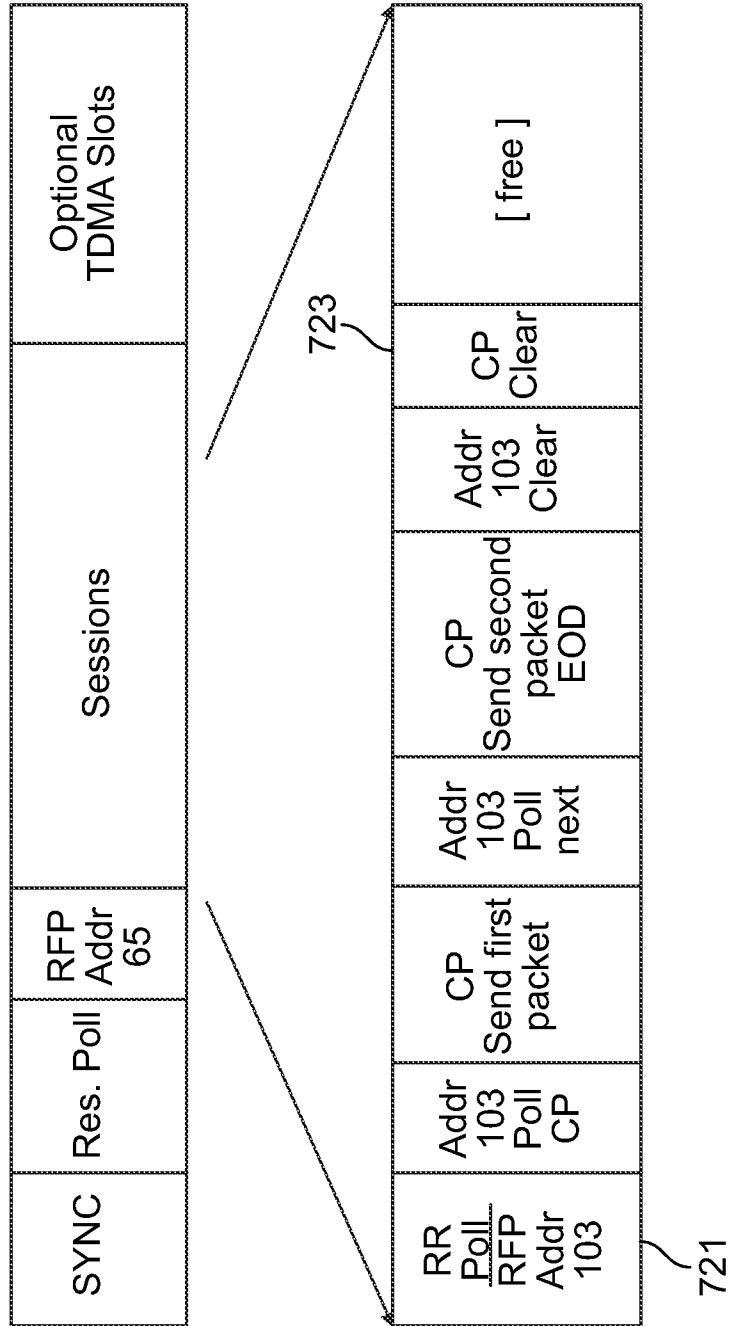


FIG. 7B

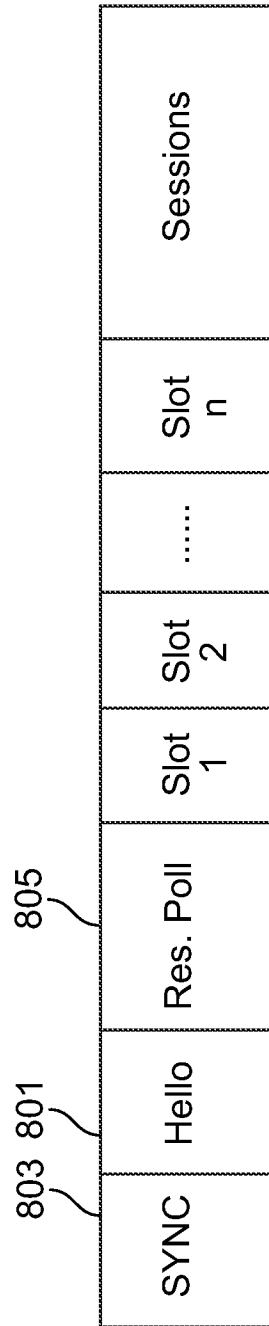


FIG. 8

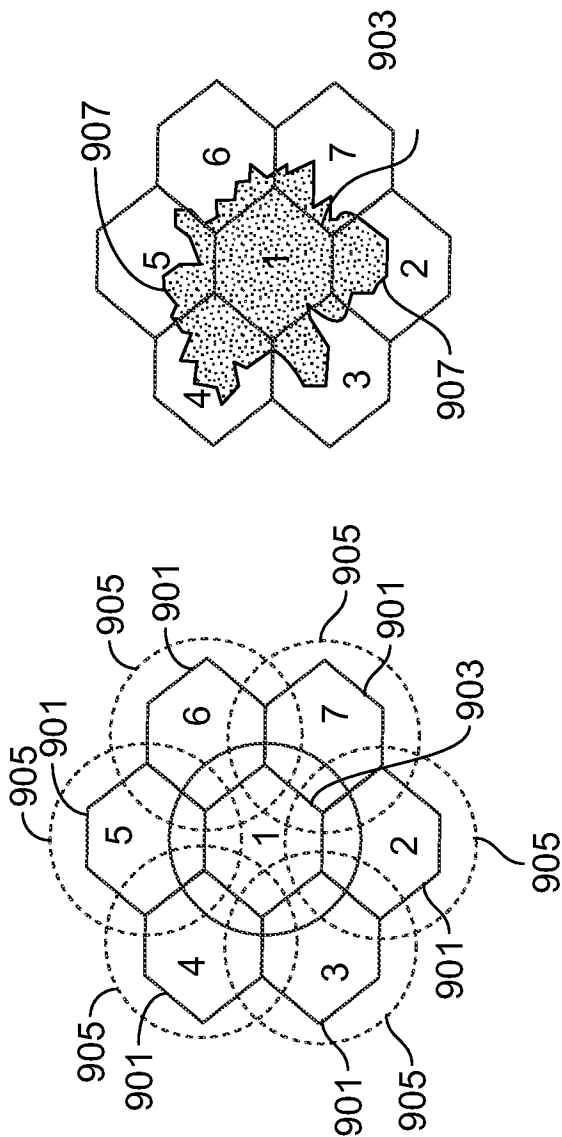
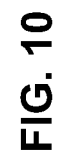


FIG. 9B

FIG. 9A



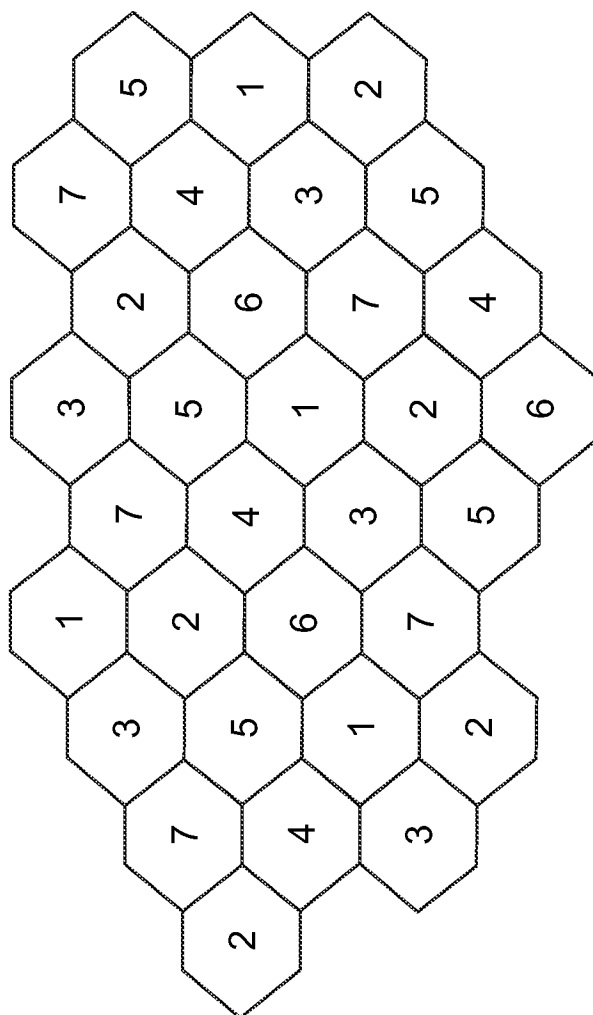


FIG. 11

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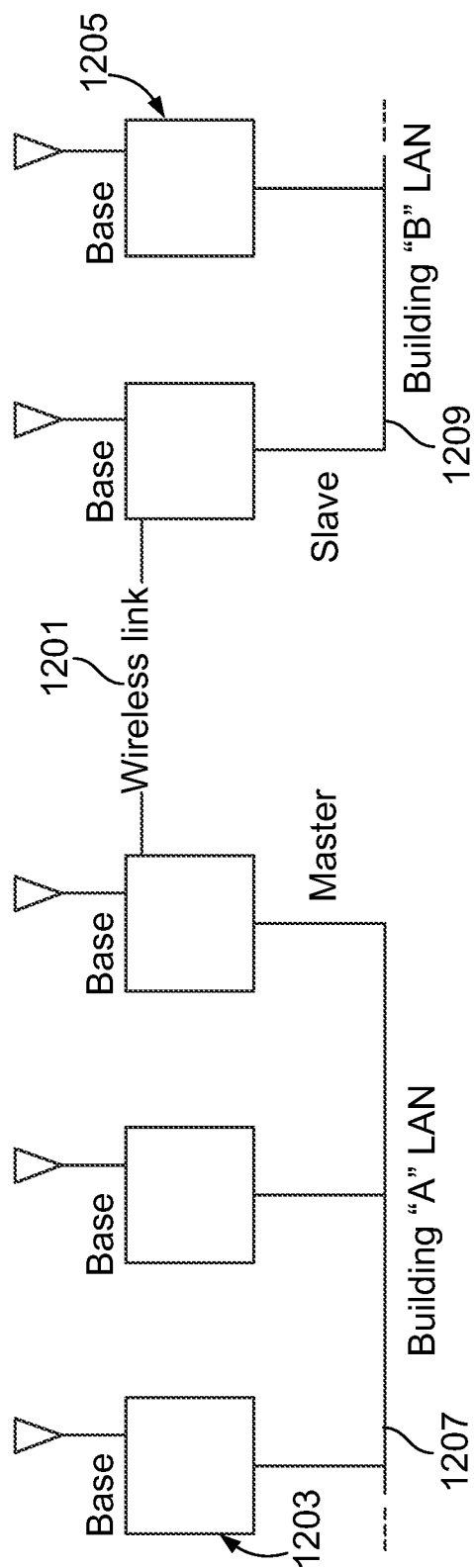


FIG. 12

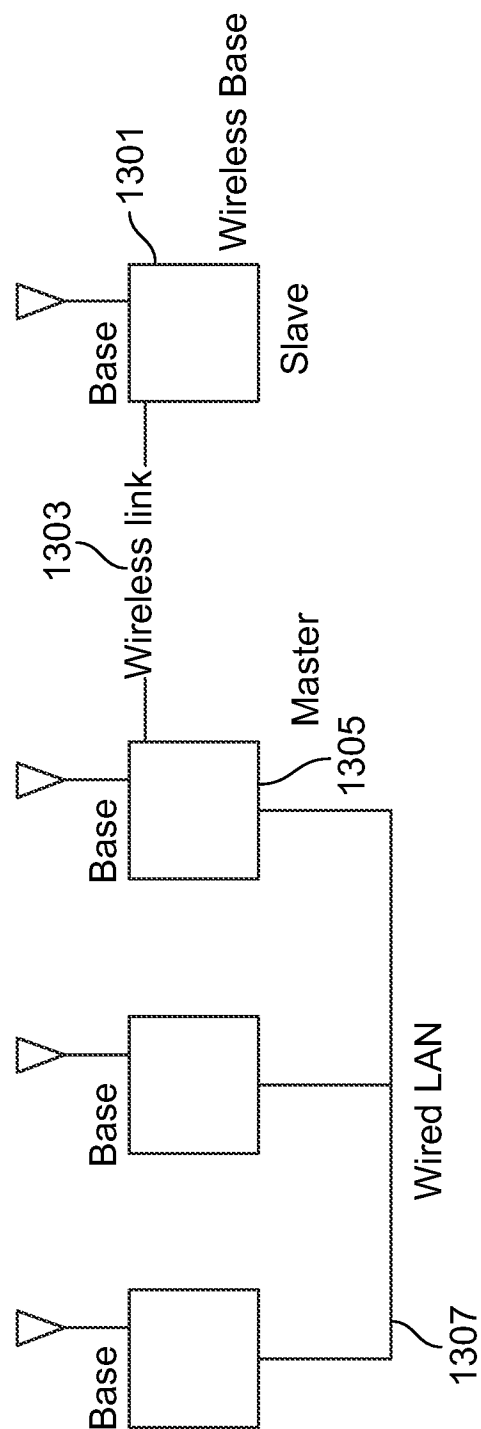


FIG. 13

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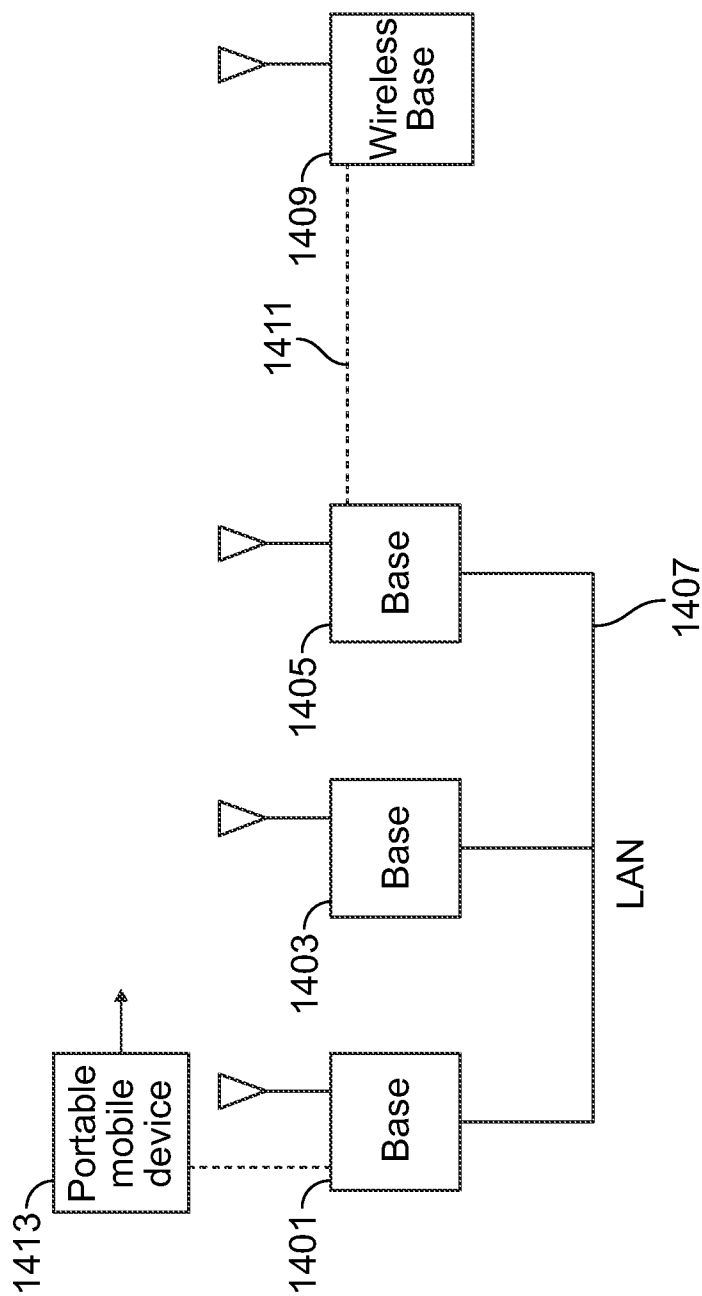


FIG. 14

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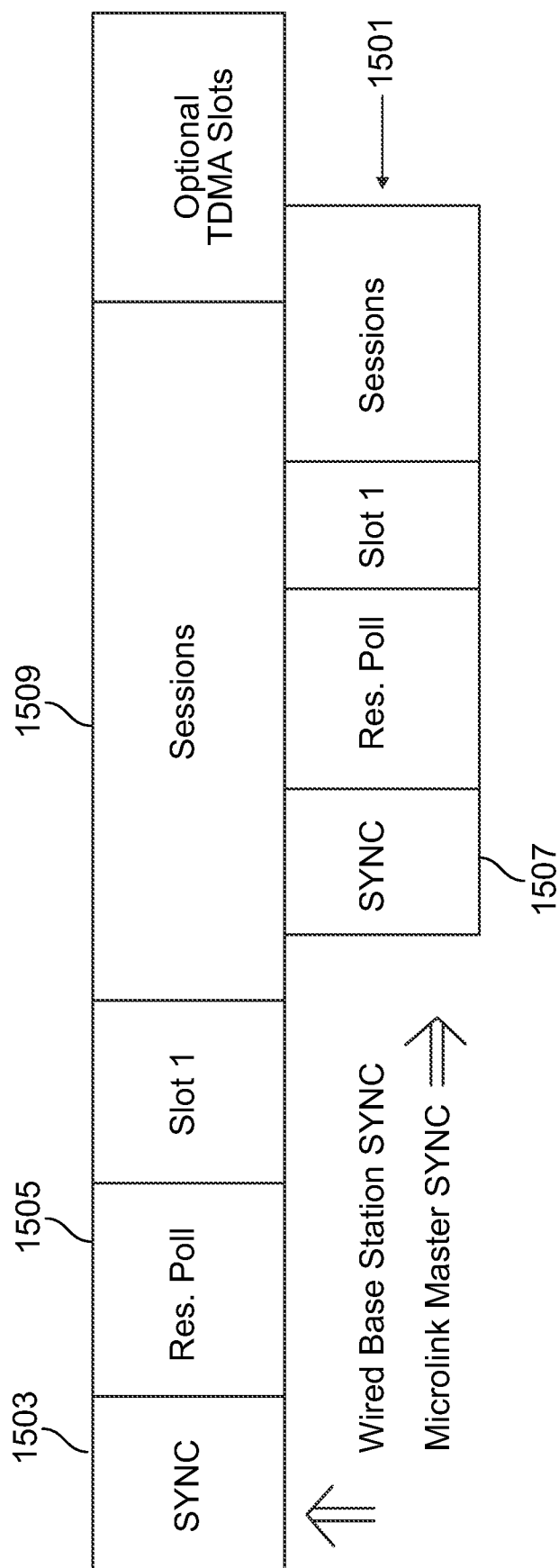


FIG. 15

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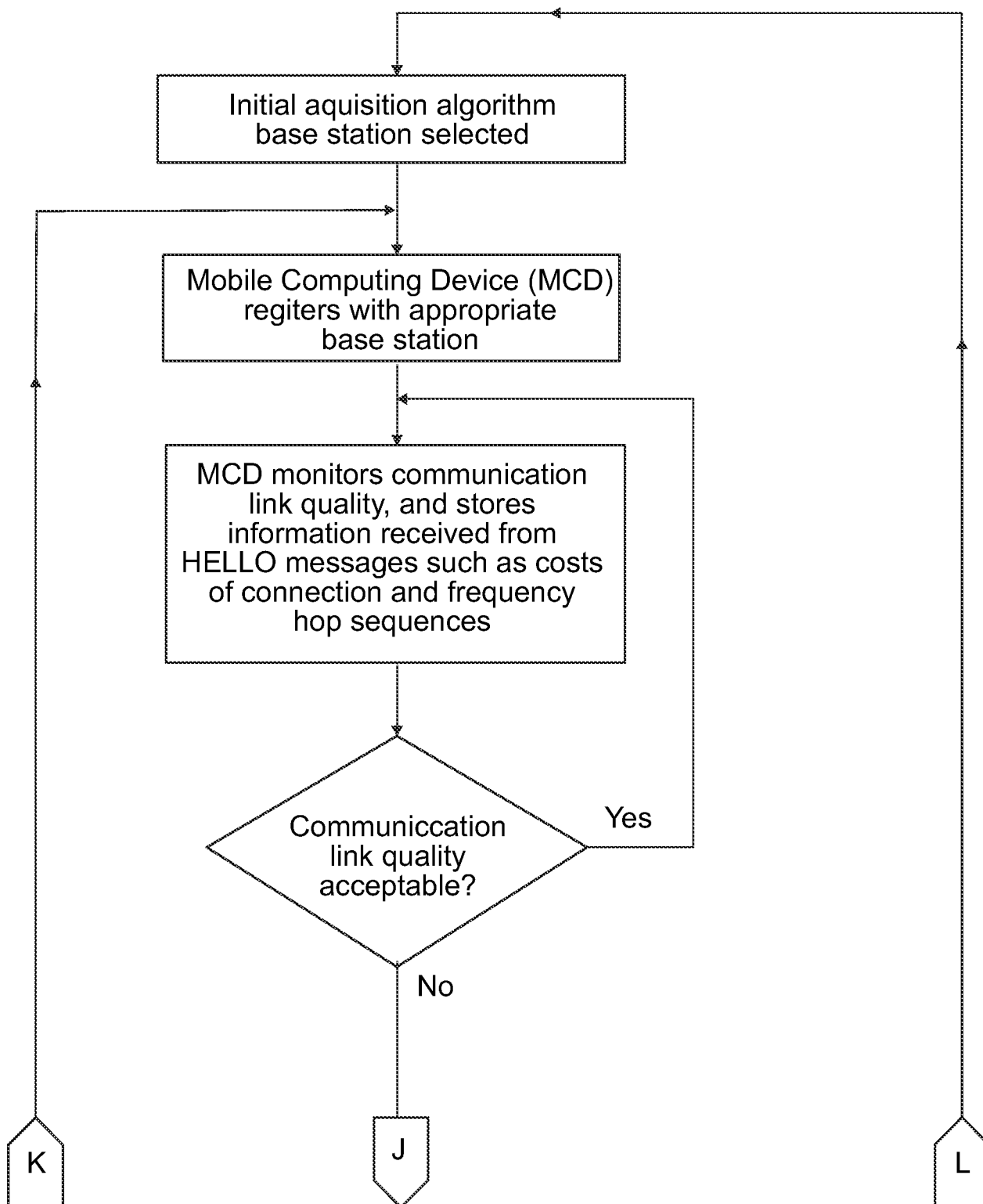


FIG. 16A

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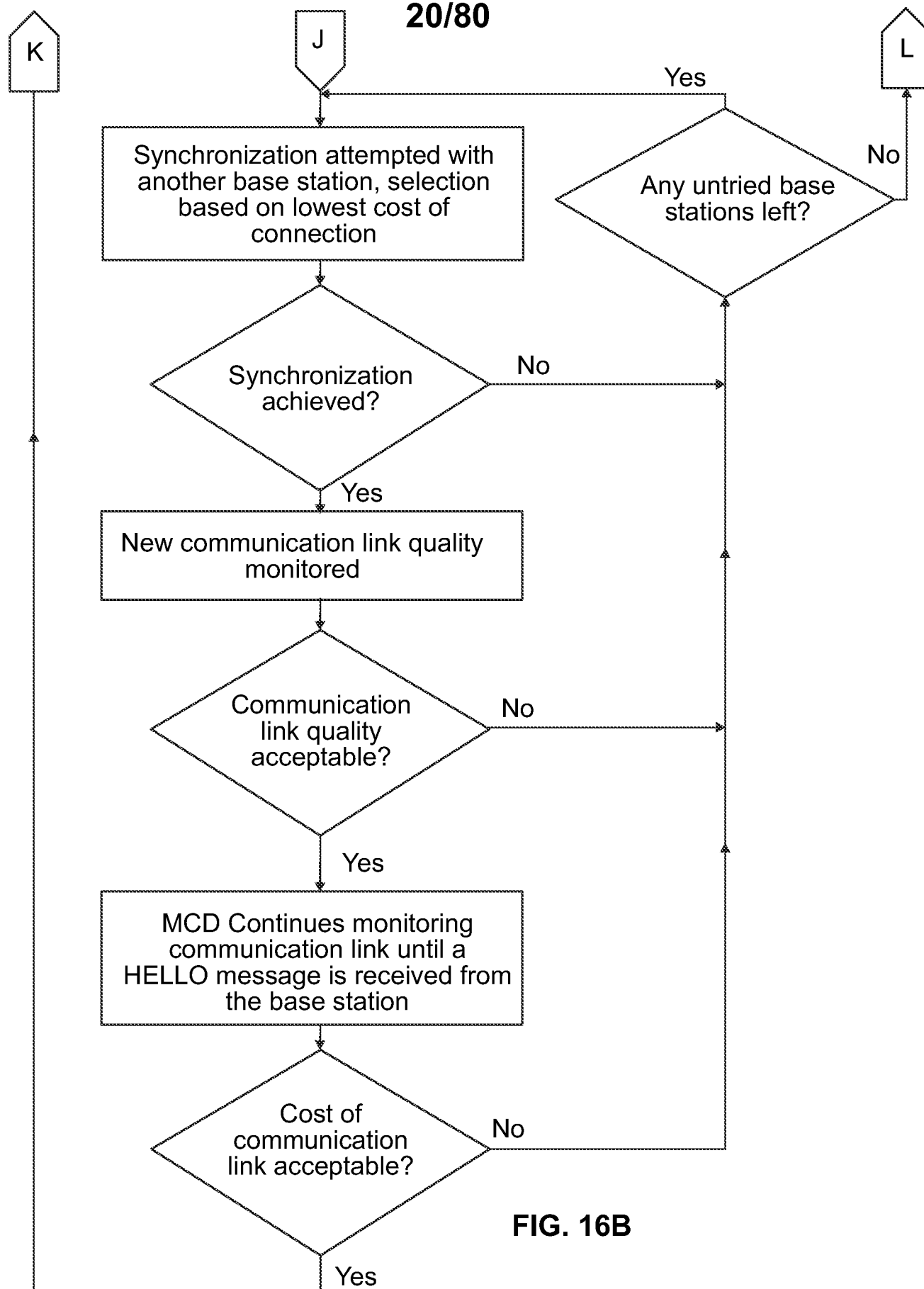


FIG. 16B

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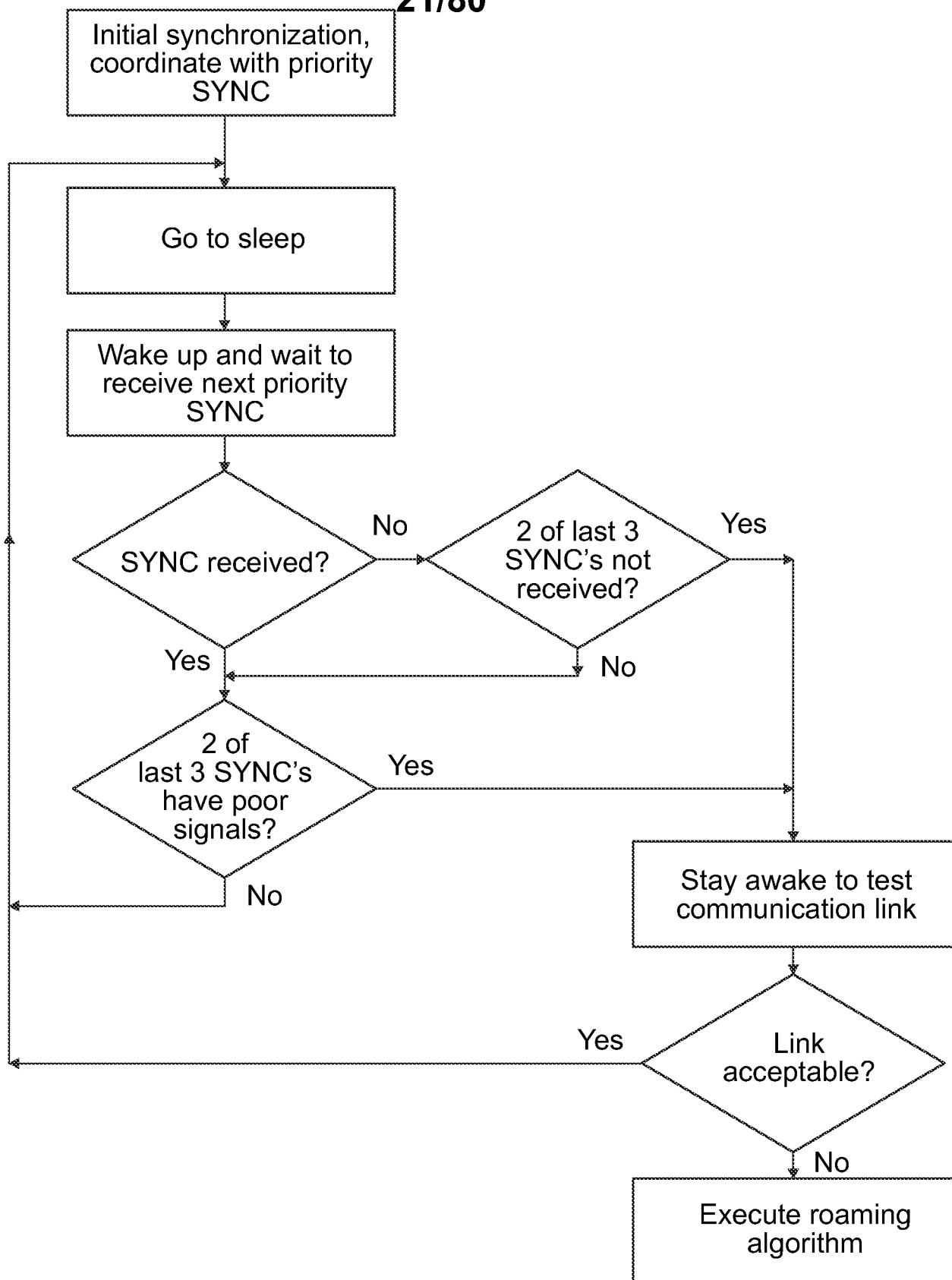


FIG. 17

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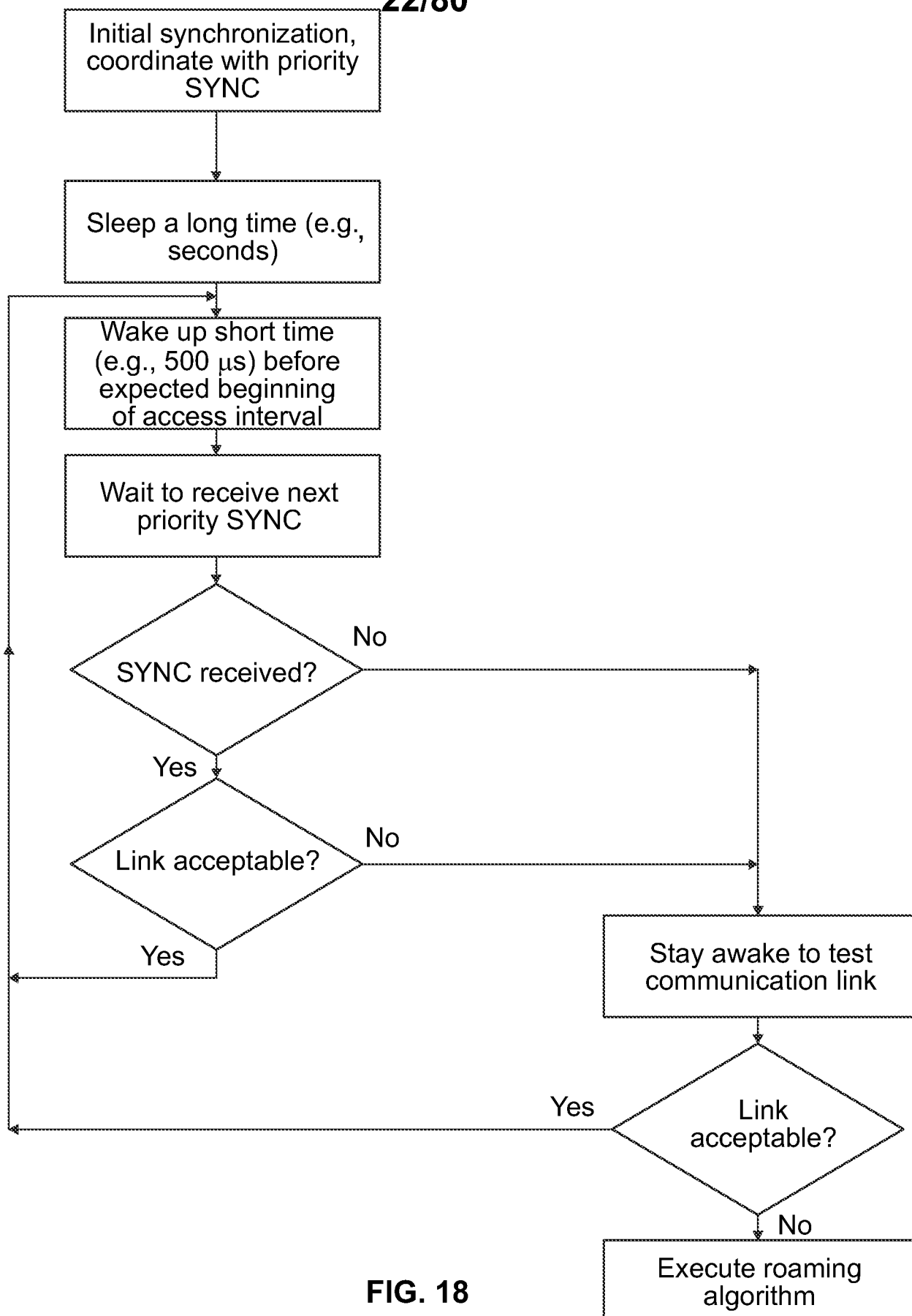


FIG. 18

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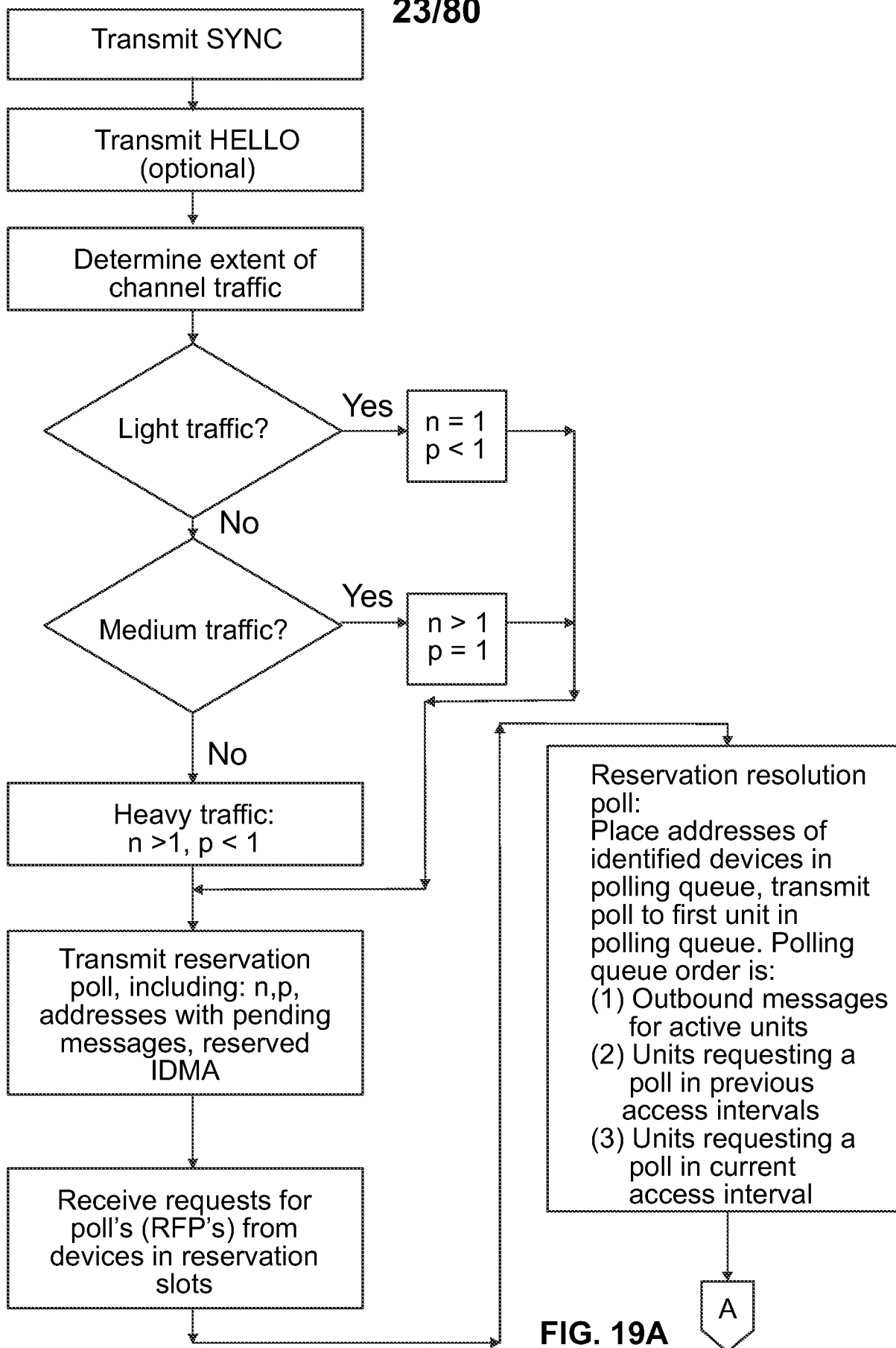


FIG. 19A

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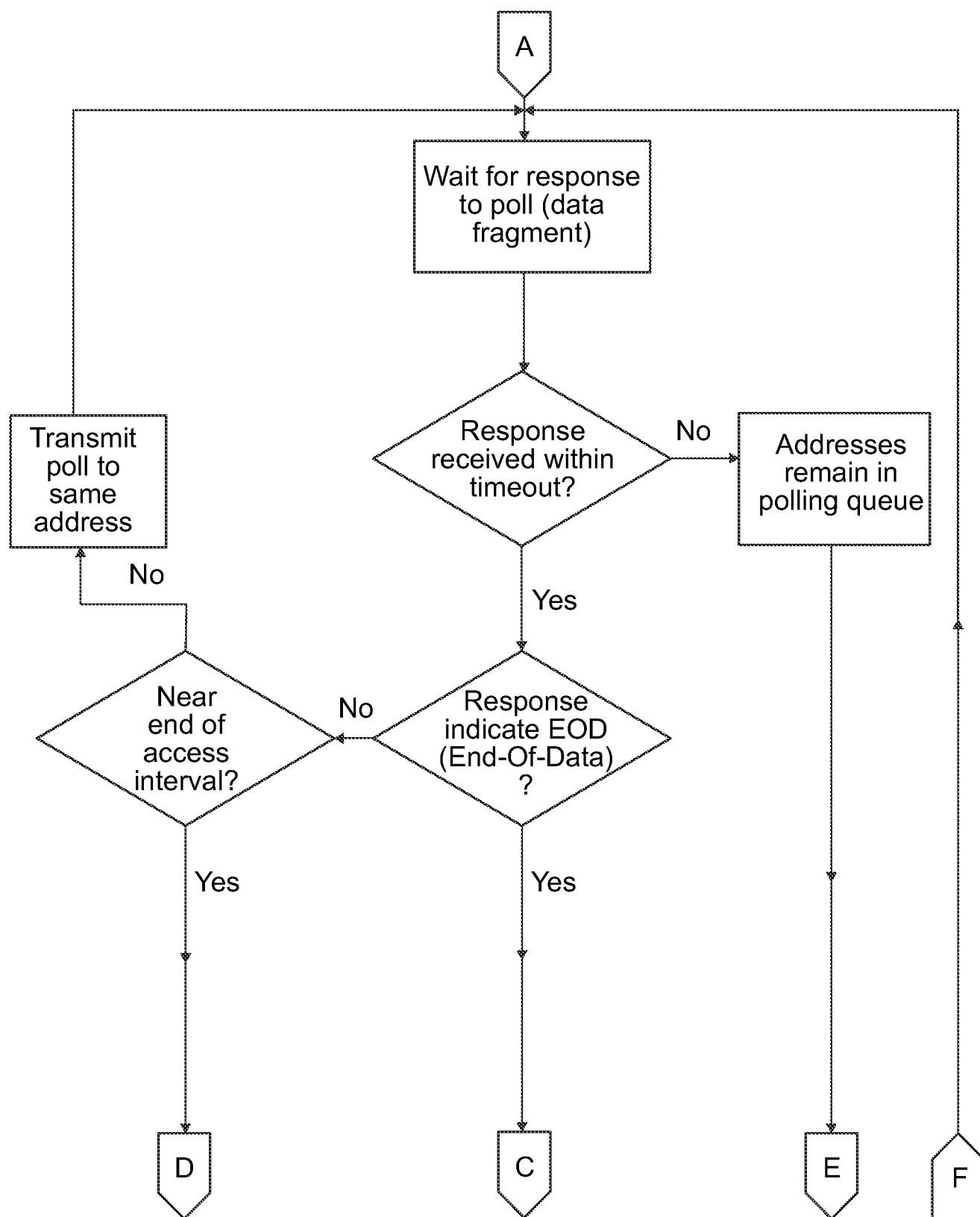


FIG. 19B-1

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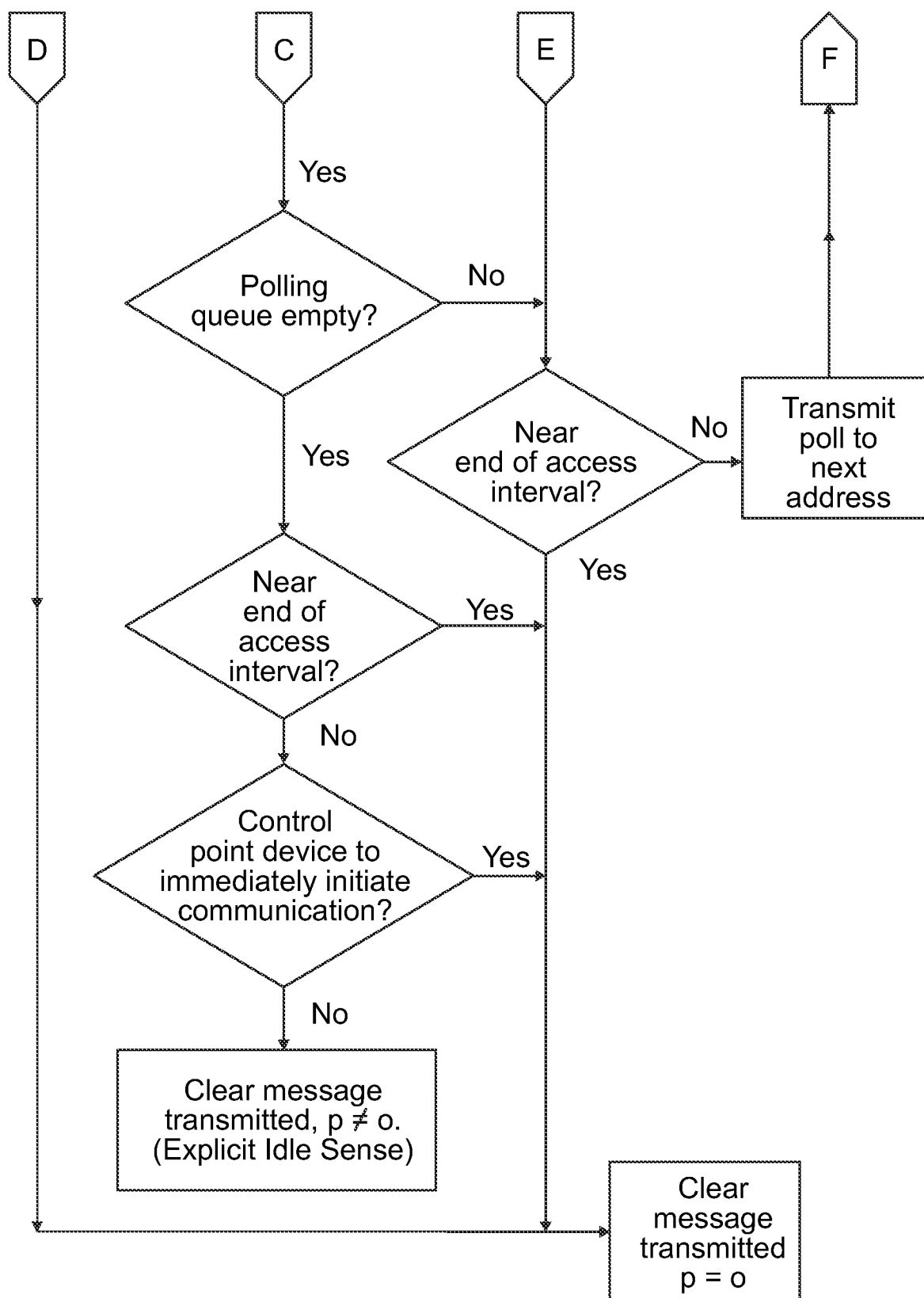


FIG. 19B-2

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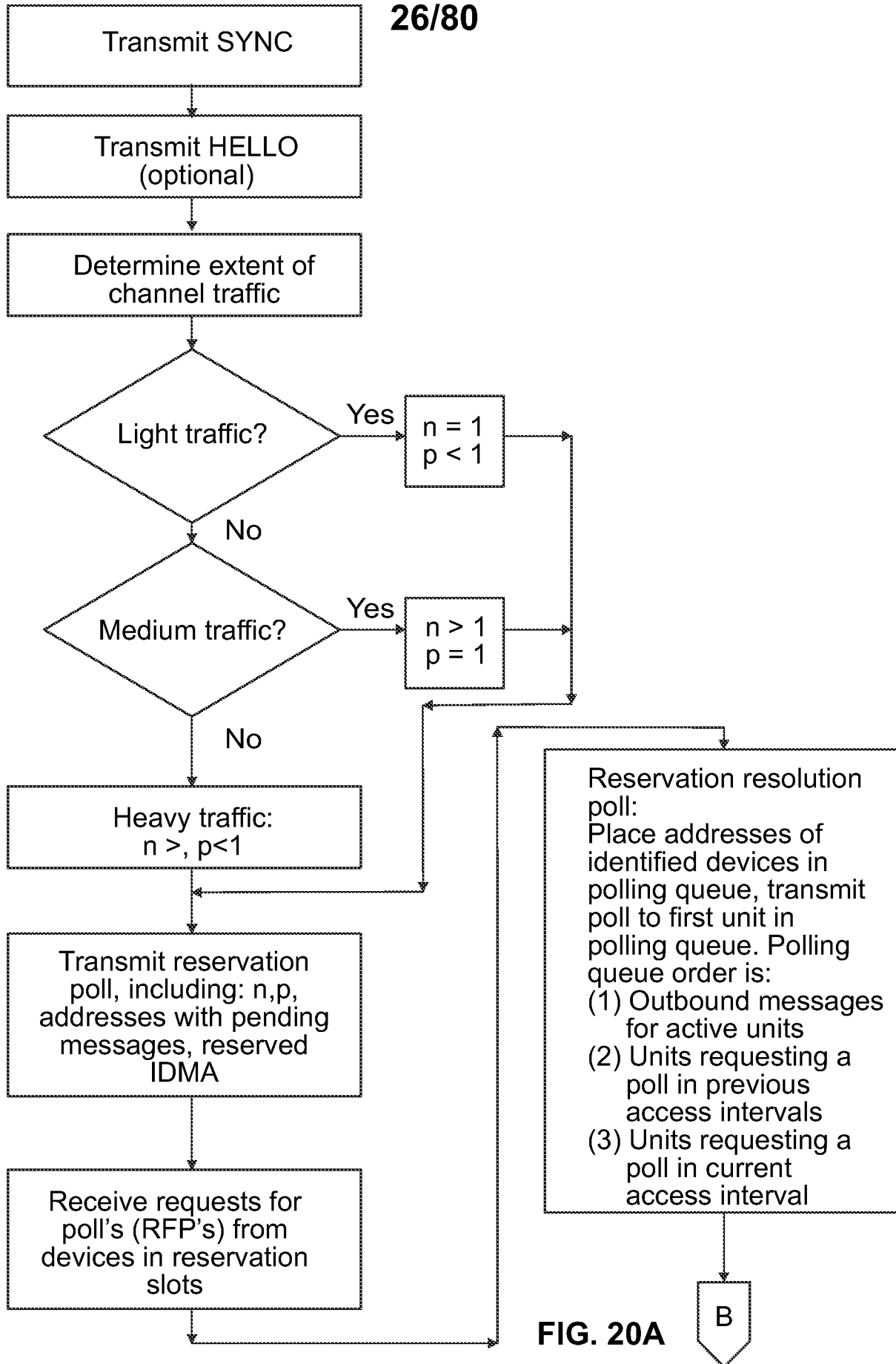


FIG. 20A

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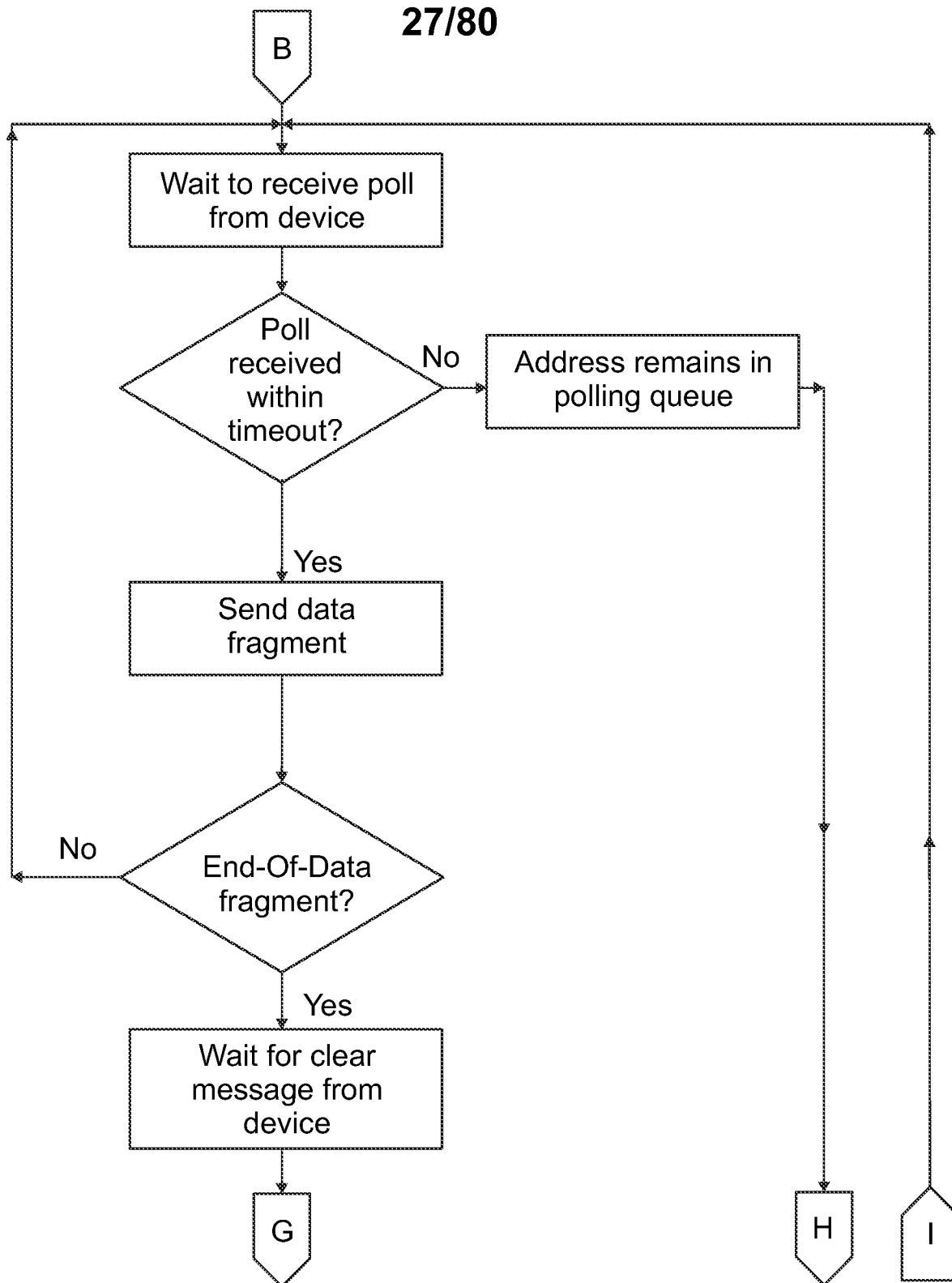


FIG. 20B-1

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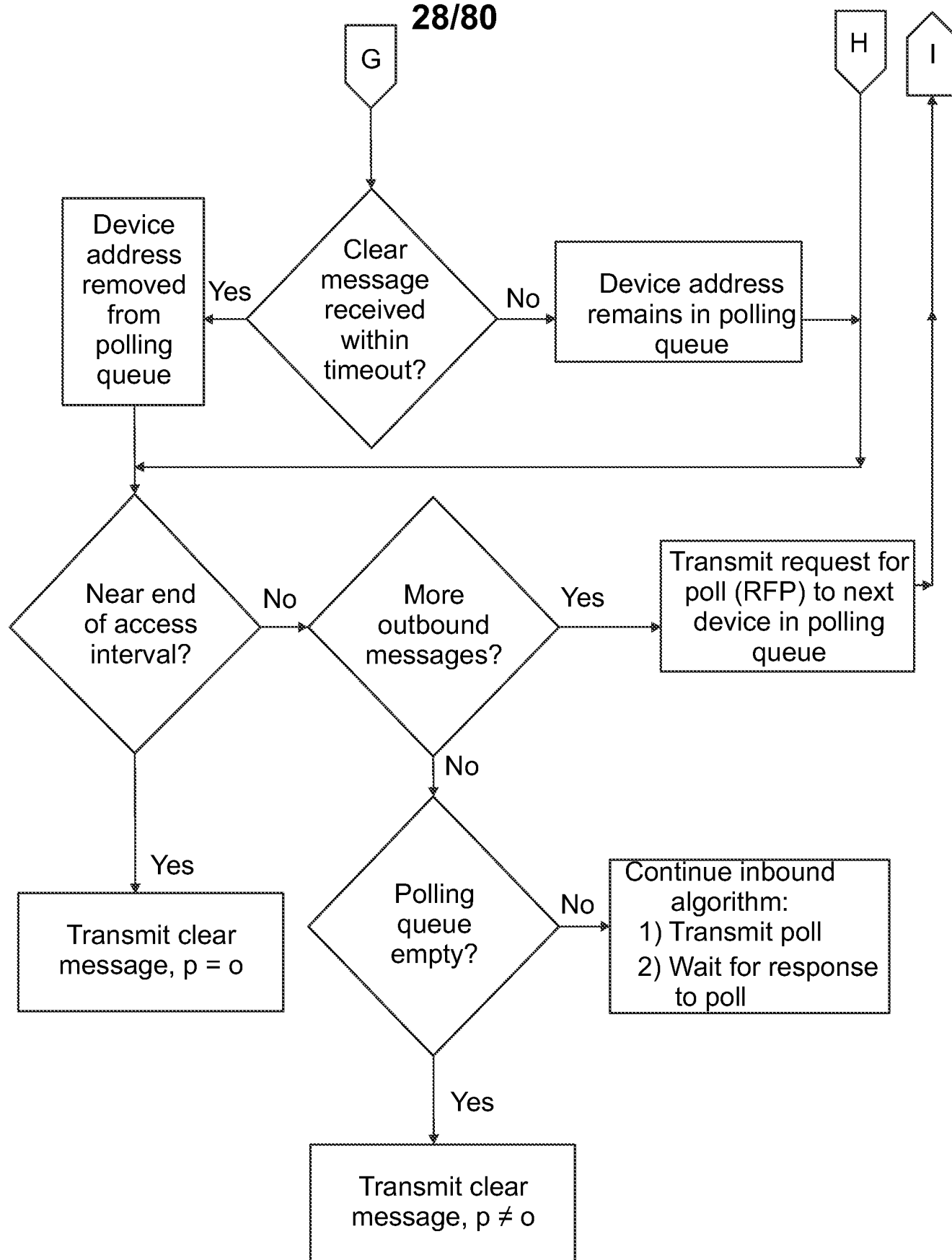


FIG. 20B-2

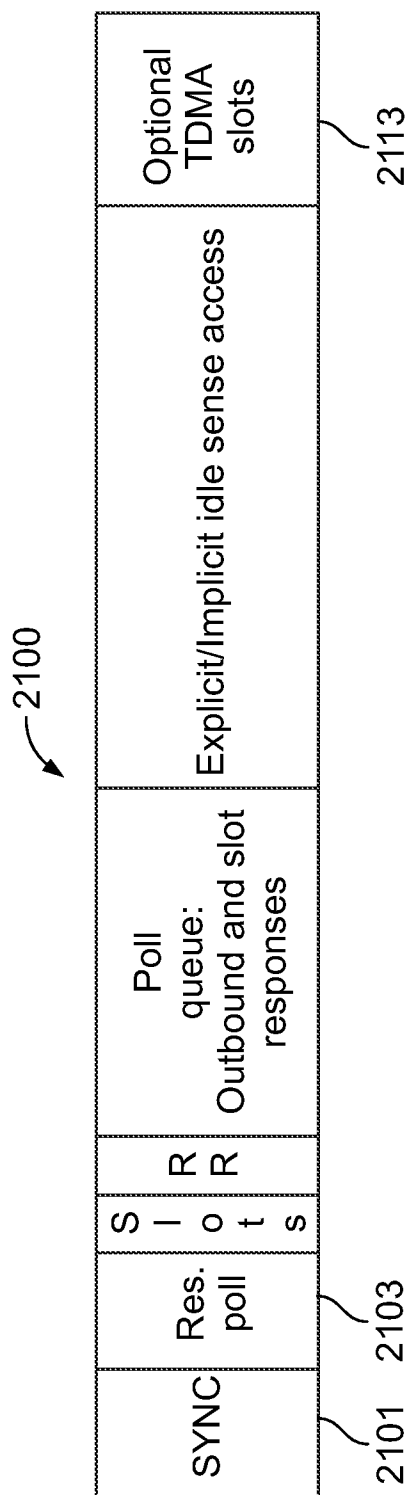


FIG. 21

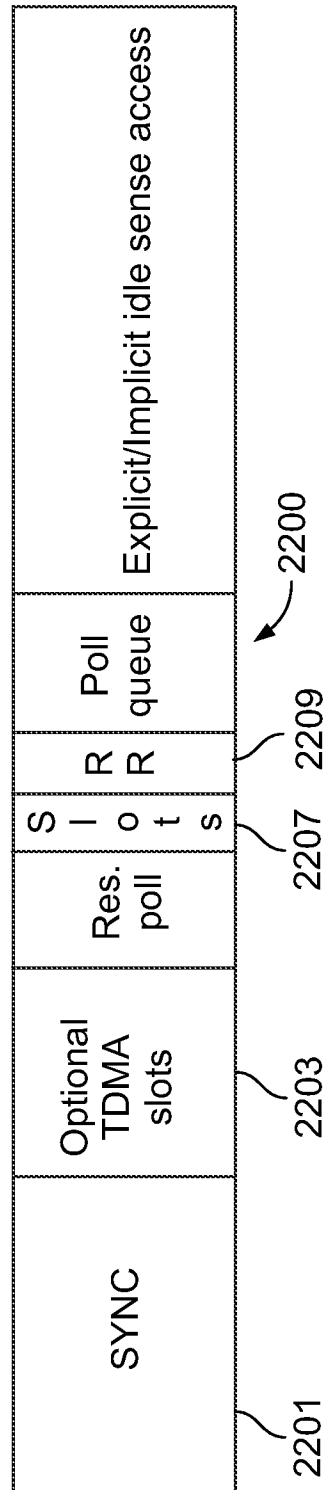


FIG. 22

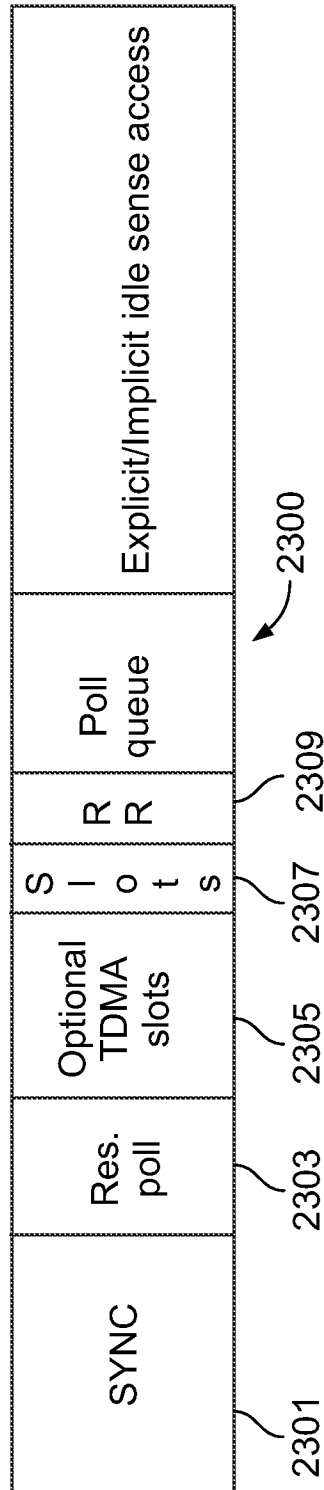


FIG. 23

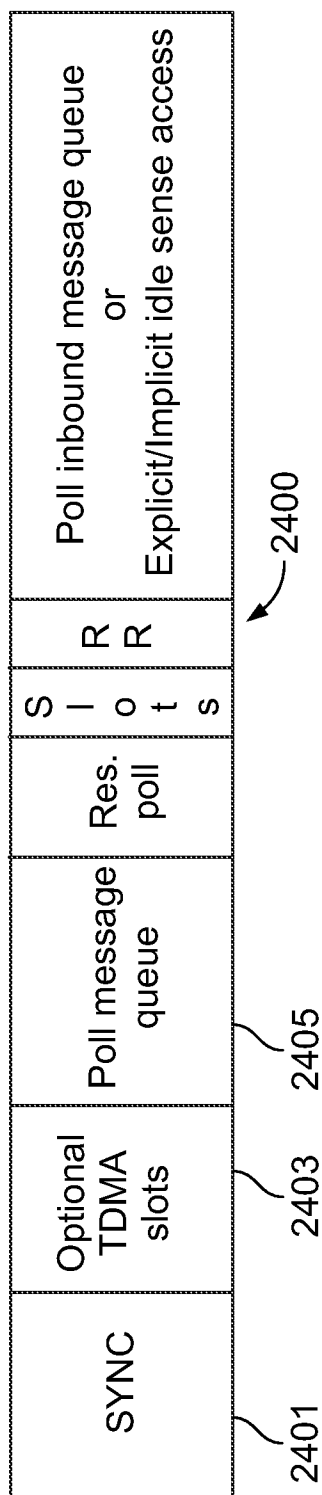


FIG. 24

- ++allows additional bit times for HDLC zero bit insertion

FIG. 25

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2600 ↗

SYNC		
Byte	Content	Definition
1	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _H)
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority SYNC, or Priority SYNC with HELLO
5	Source Address 1	Control Point Address
6	Source Address 2	Control Point Address 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	Net ID
9*	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10*	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Slots
		3 Bits Number of Available Time Slots
11*	Random	Randomly Selected Character for Improved Encryption
12	CRC 1	Error Check
13	CRC 2	Error Check
14	FLAG	Frame Delimiter

*Optionally Encrypted Bytes

FIG. 26

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SYNC

2700 ↗

Byte	Content	Definition
1	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (RES = Broadcast = FFFF _h)
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type = RES POLL
5	SLOT PARMS	3 Bits Number of Slots
6		5 Bits Probability Factor
7*	Sequence	Hopping Sequence ID
8*	Index	Hopping Sequence Index
9*	NET PARMS	
		3 Bits Hop Rate
		2 Bits A's per Hop
		3 Bits Reserved
10	CRC 1	Error Check
11	CRC 2	Error Check
12	FLAG	Frame Delimiter

FIG. 27

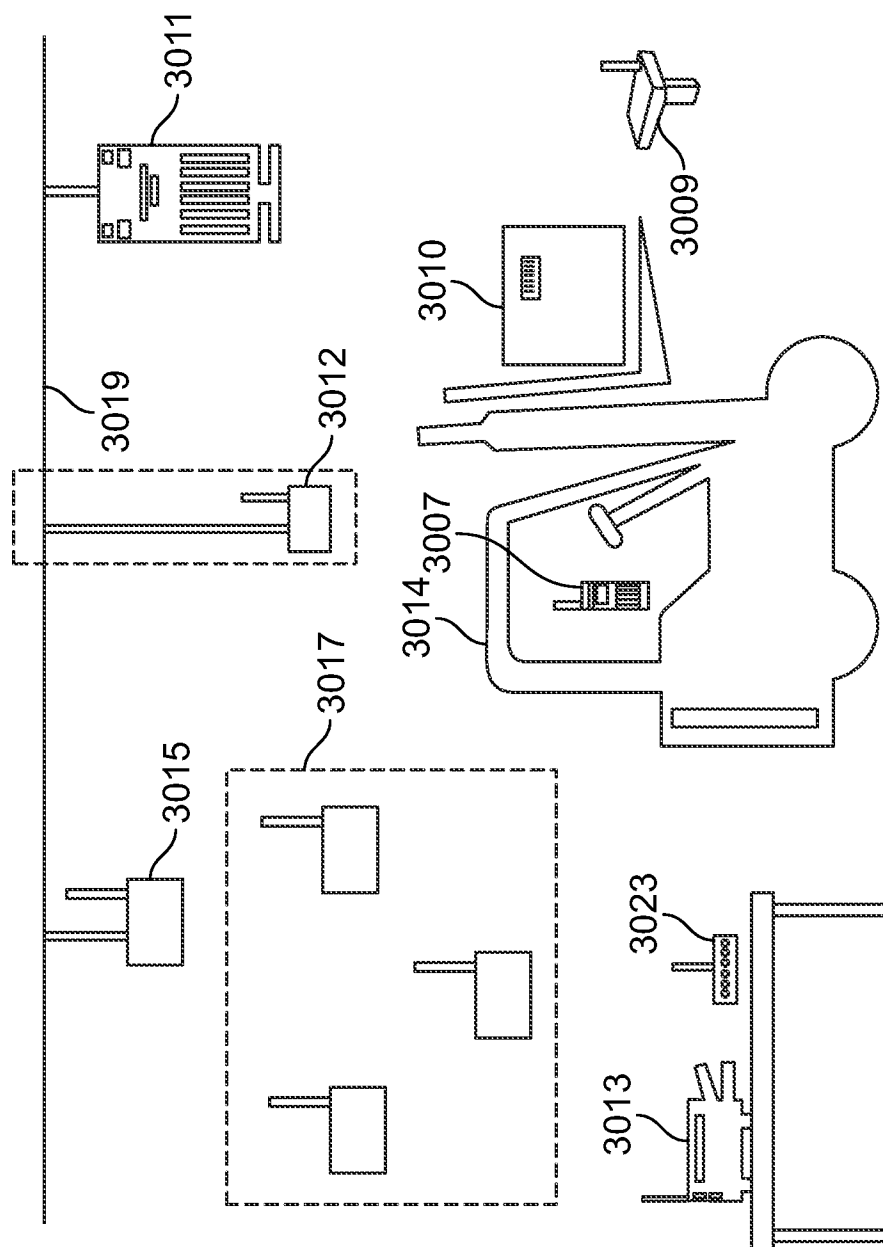


FIG. 28A

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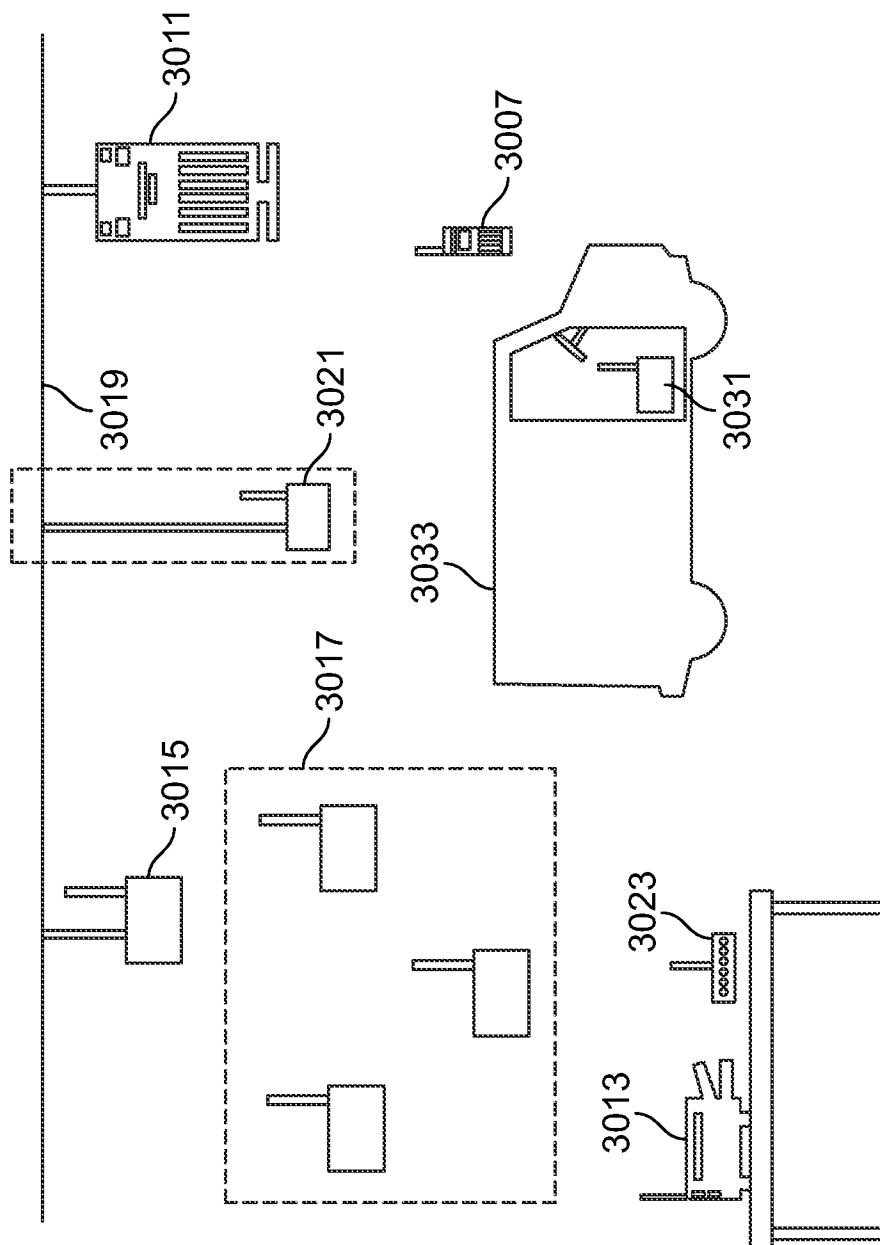


FIG. 28B

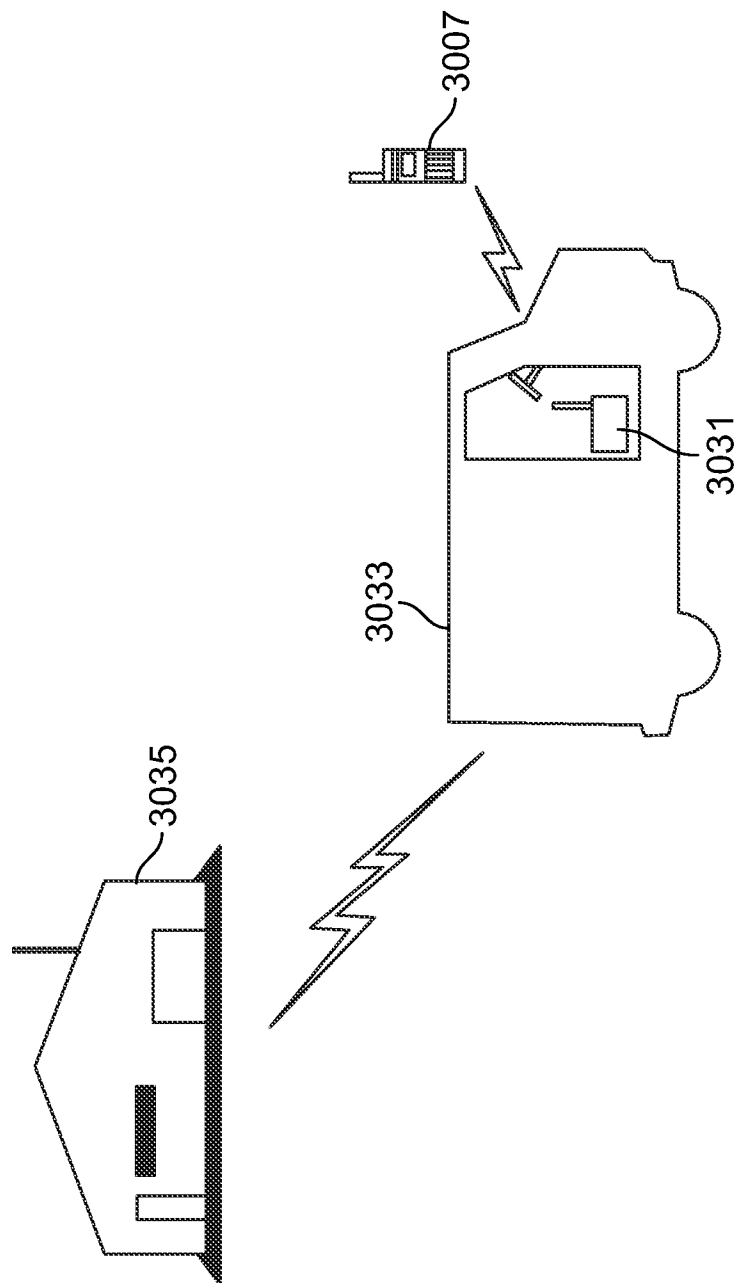


FIG. 28C

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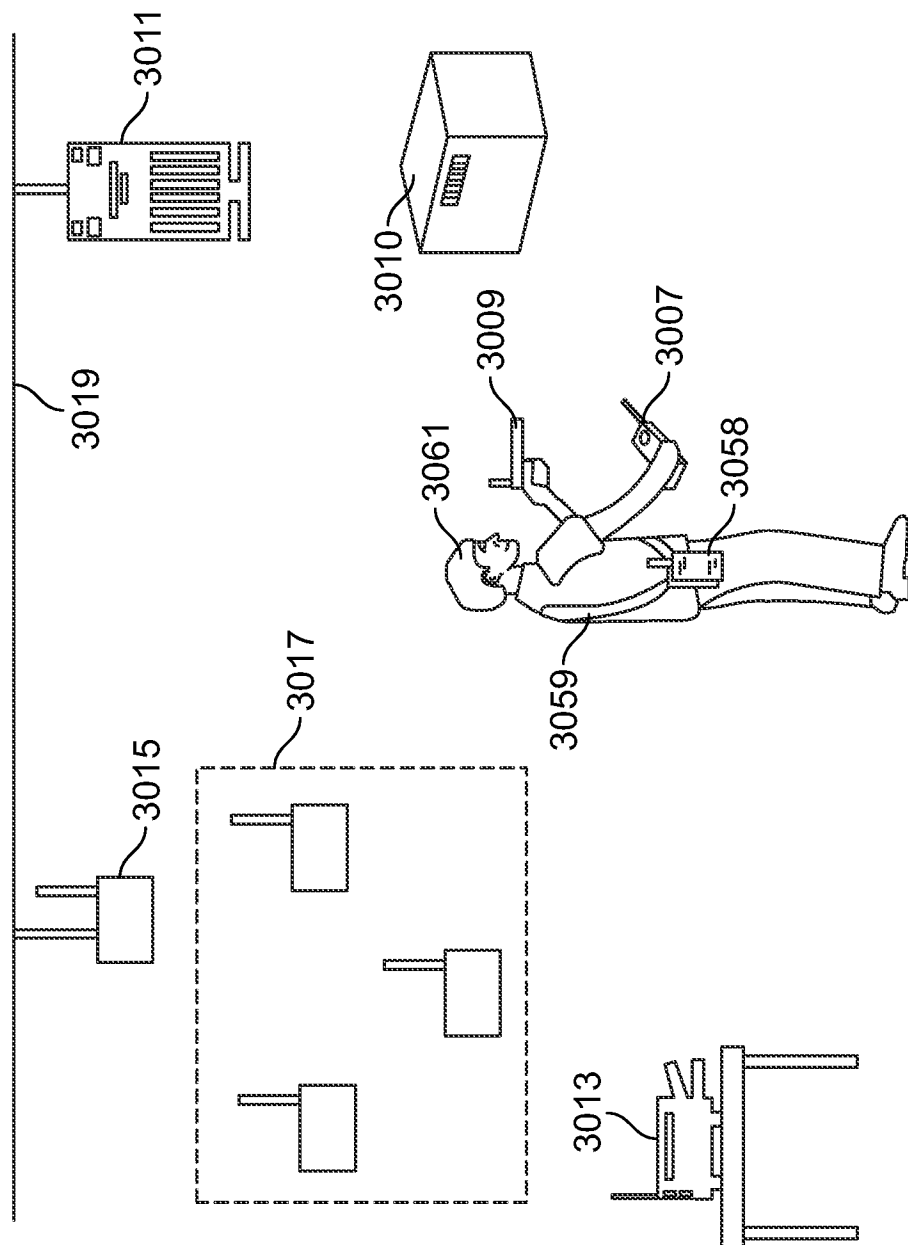


FIG. 29A

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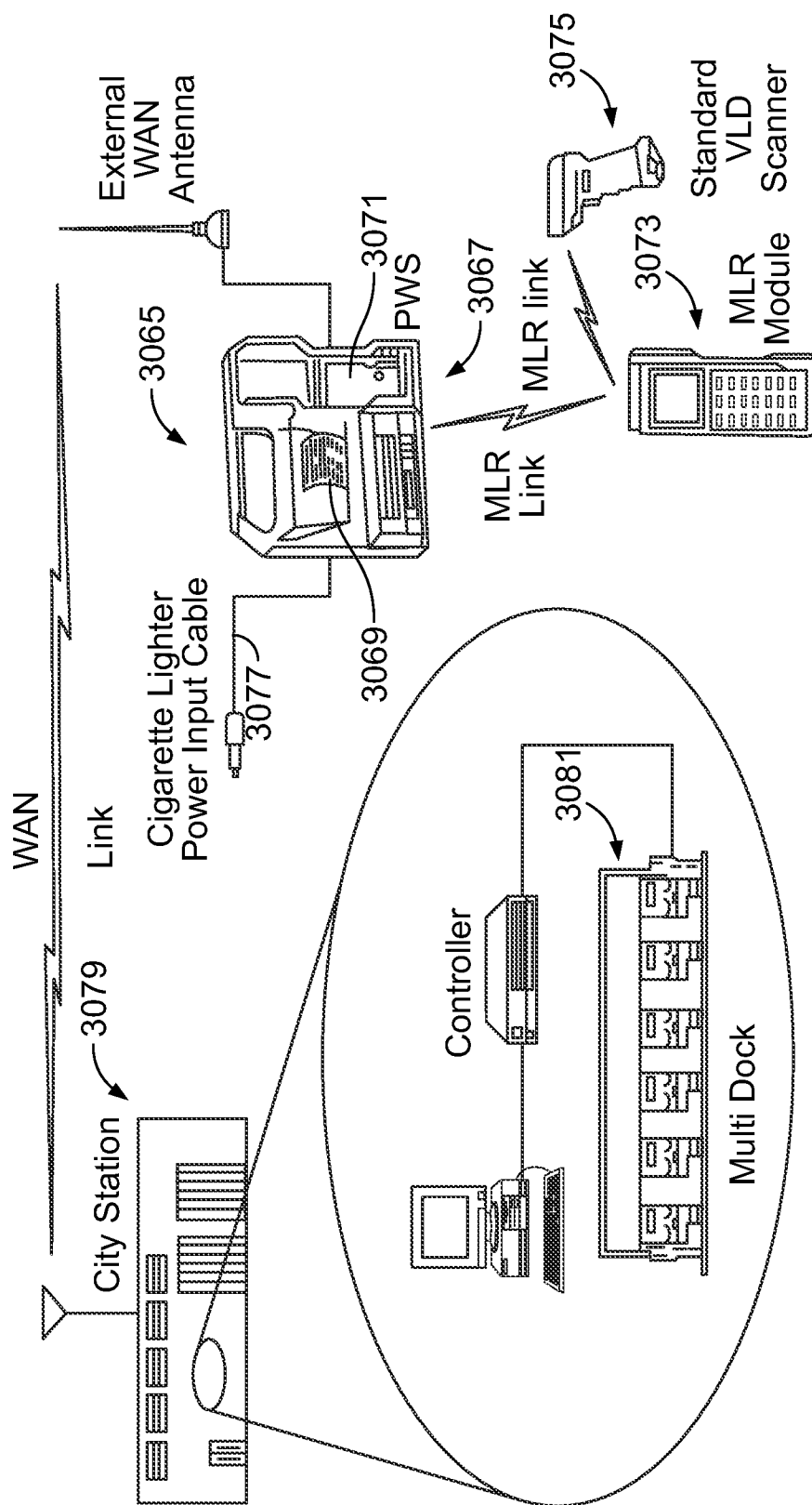


FIG. 29B

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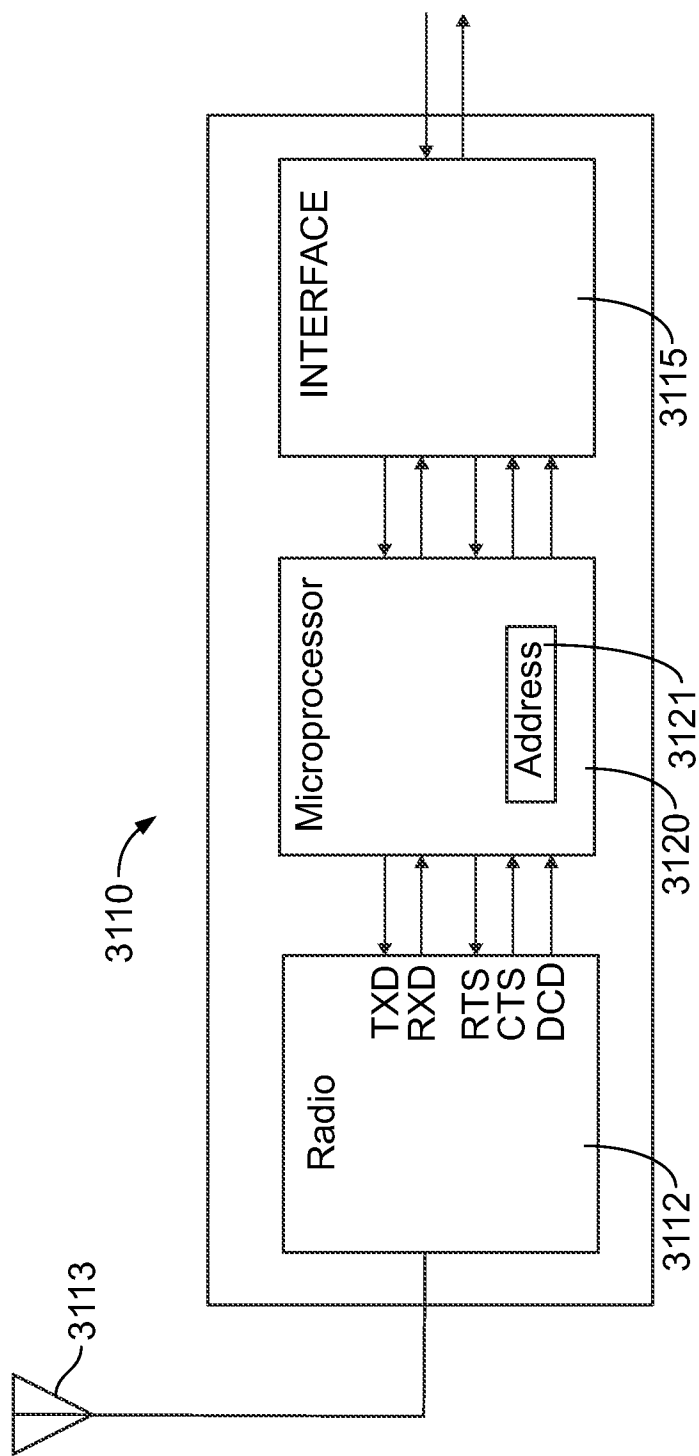
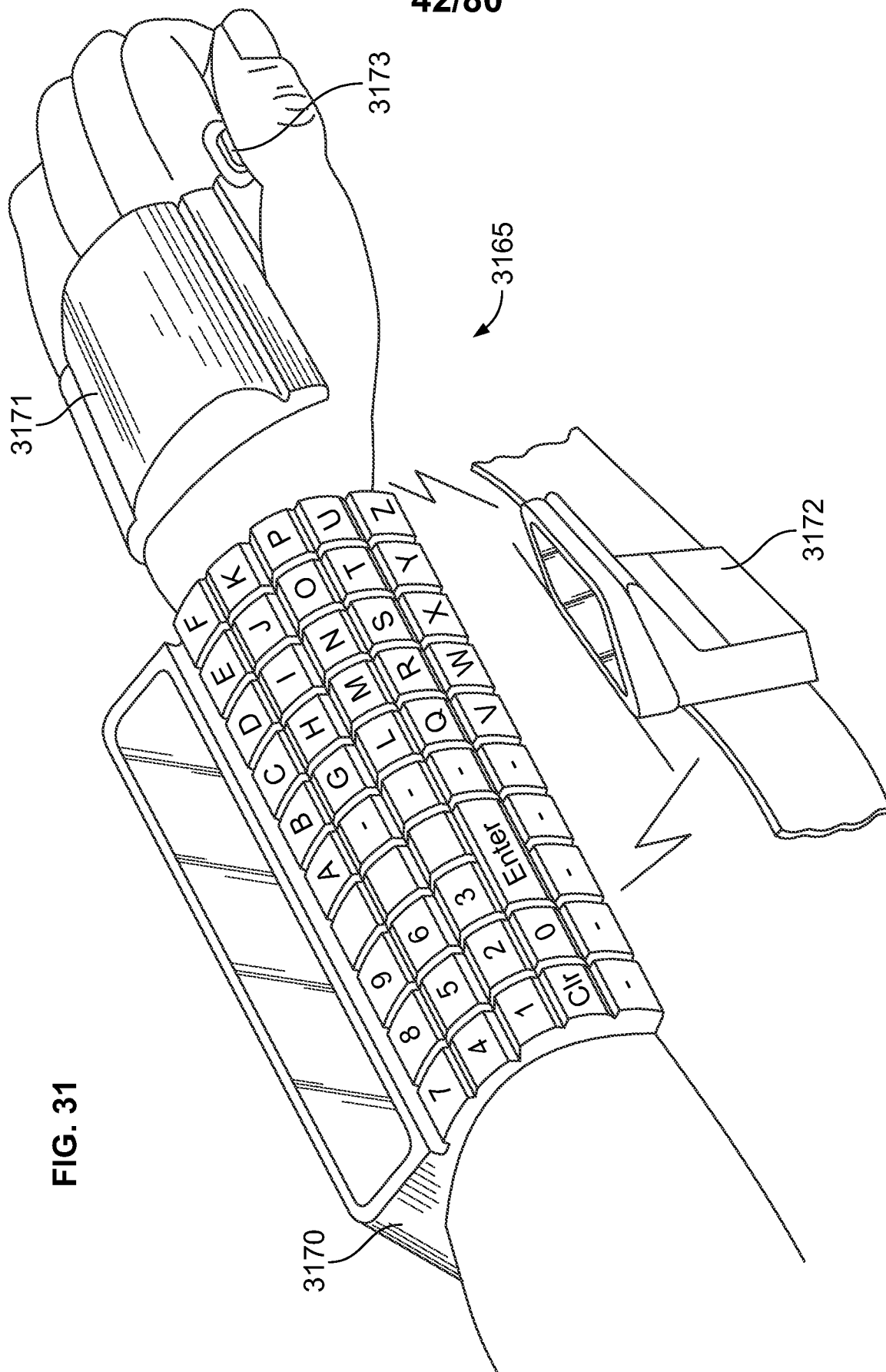


FIG. 30

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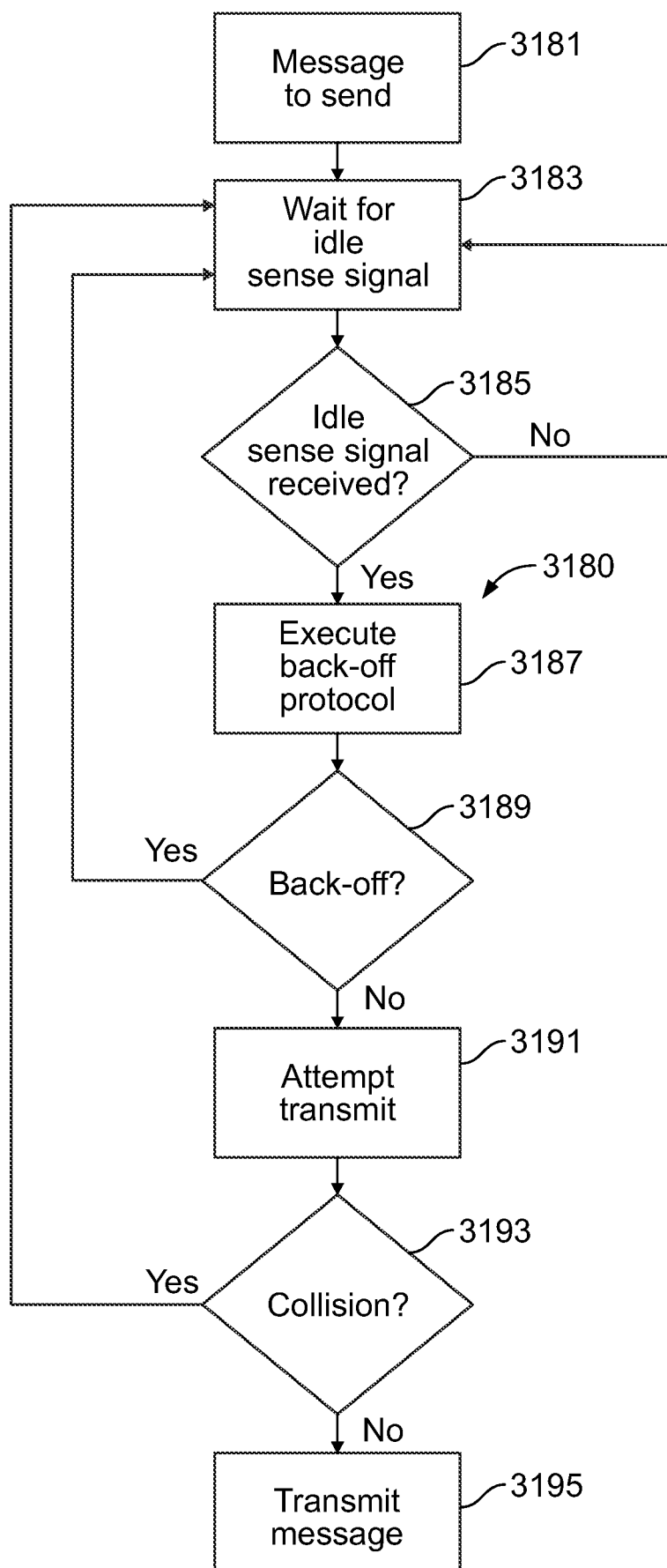


FIG. 32

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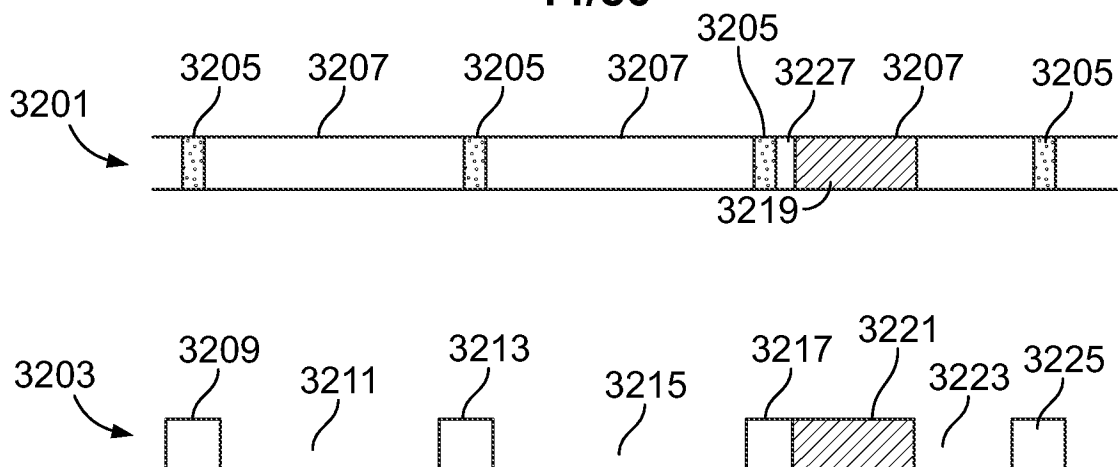


FIG. 33A

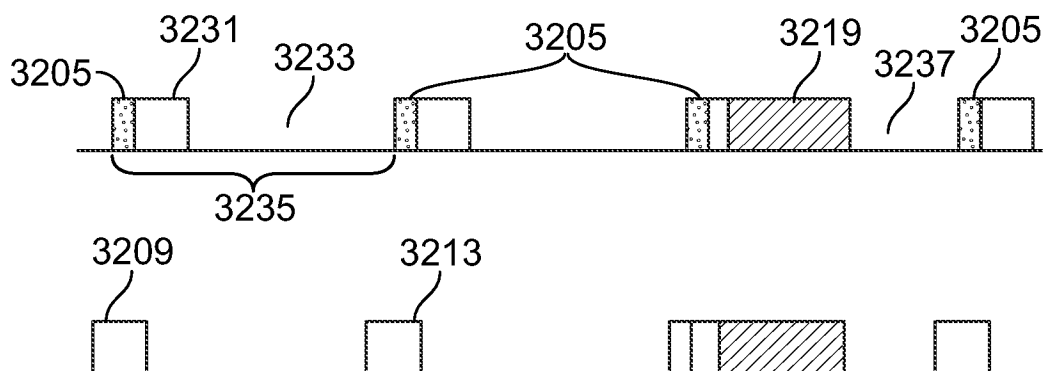


FIG. 33B

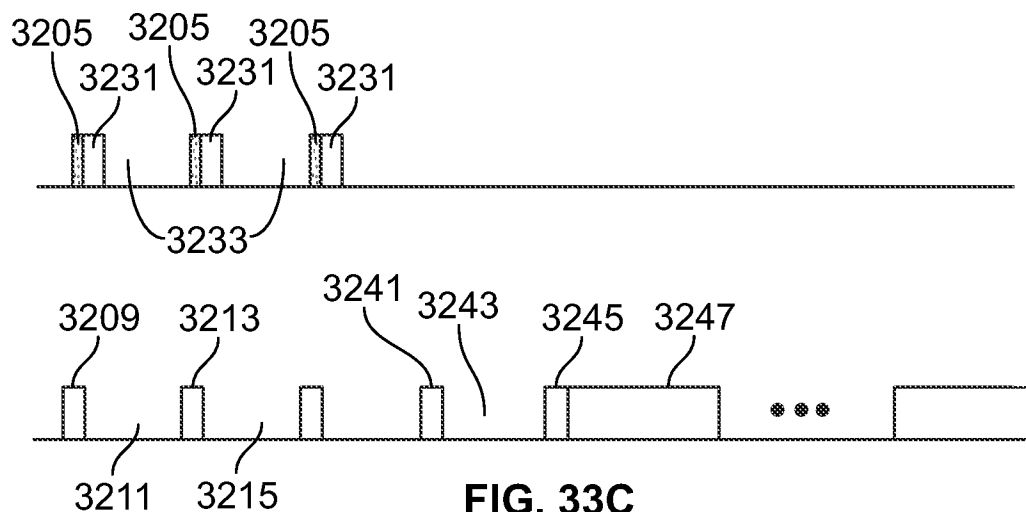


FIG. 33C

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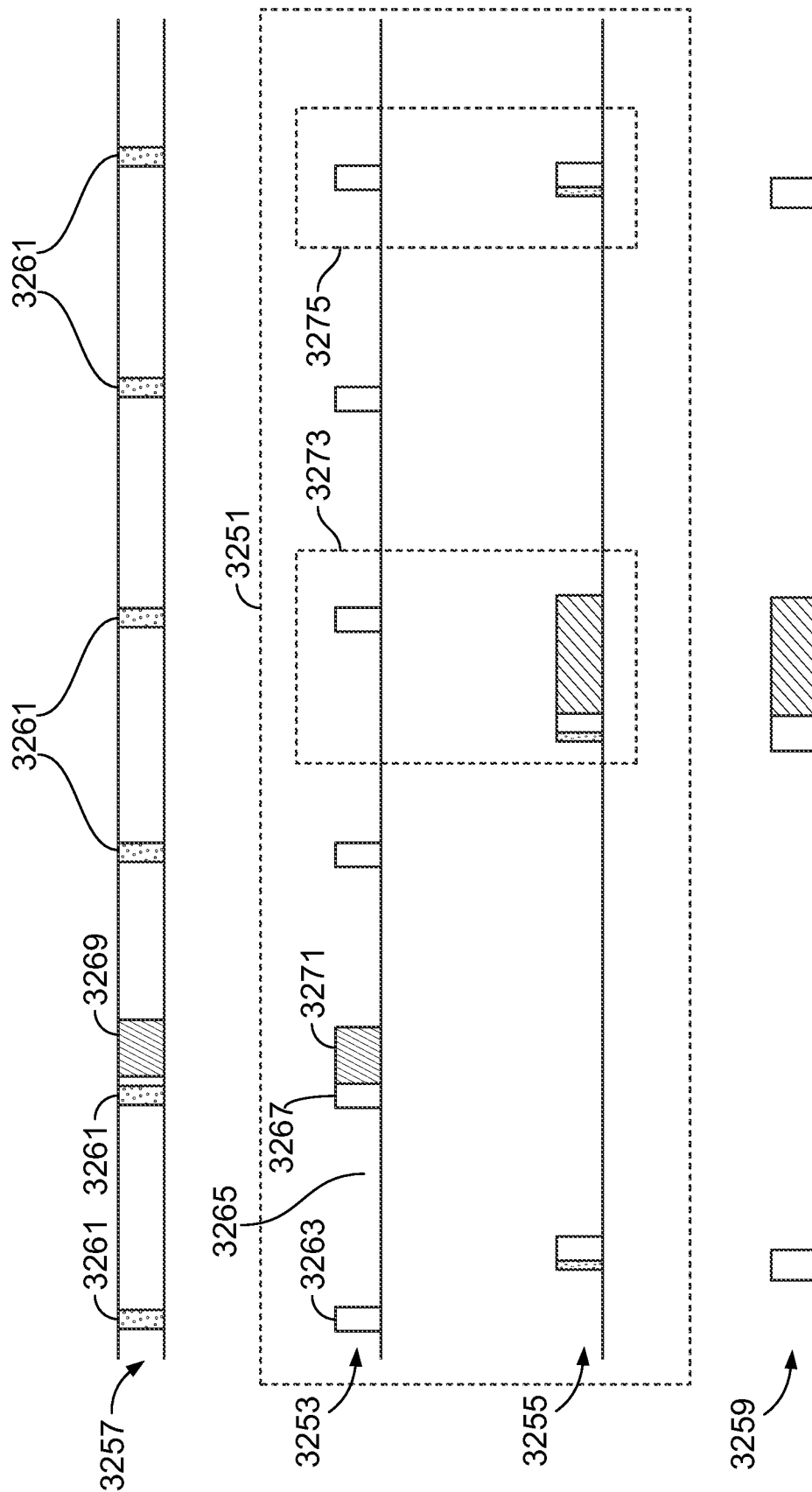
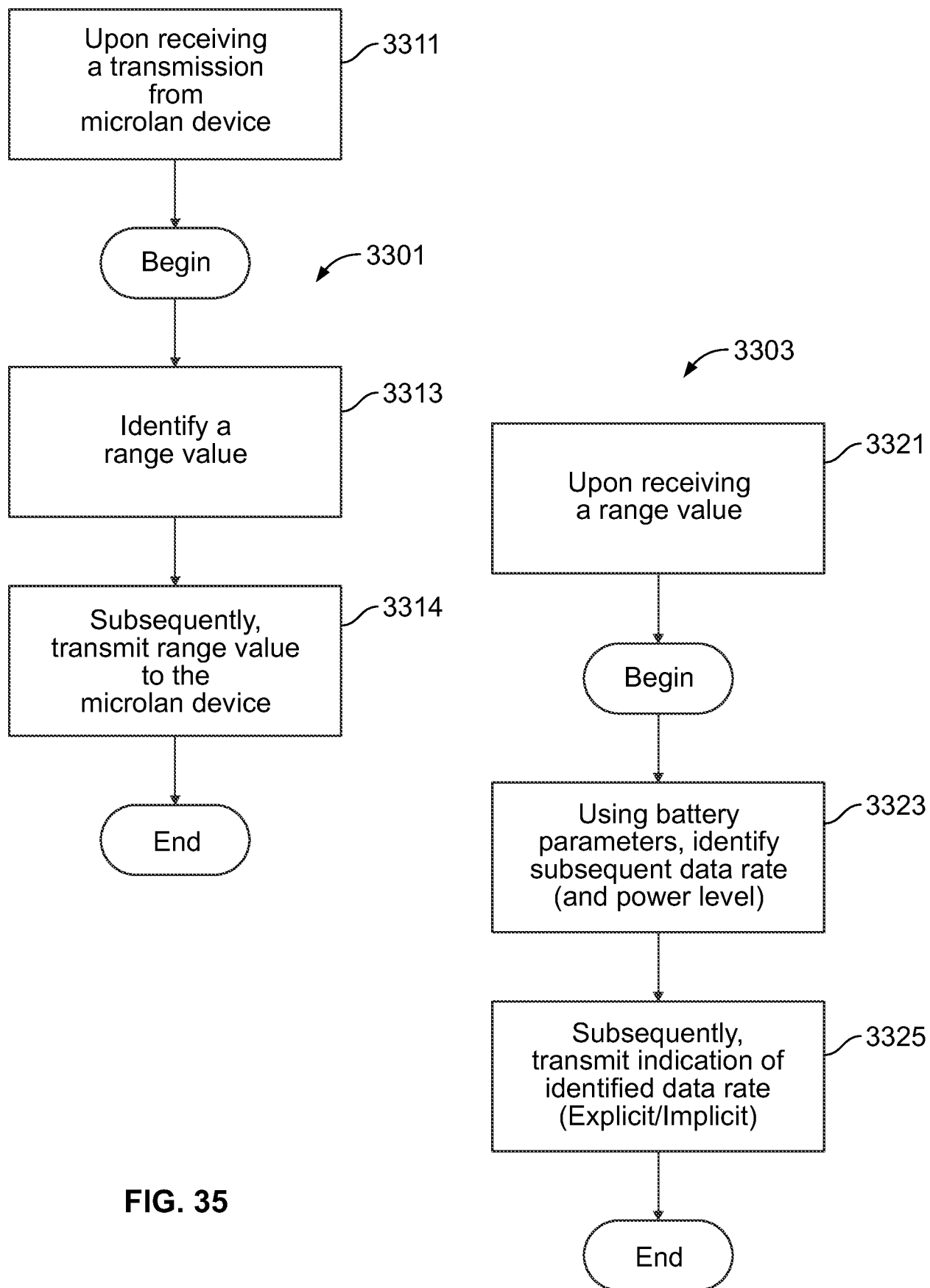


FIG. 34

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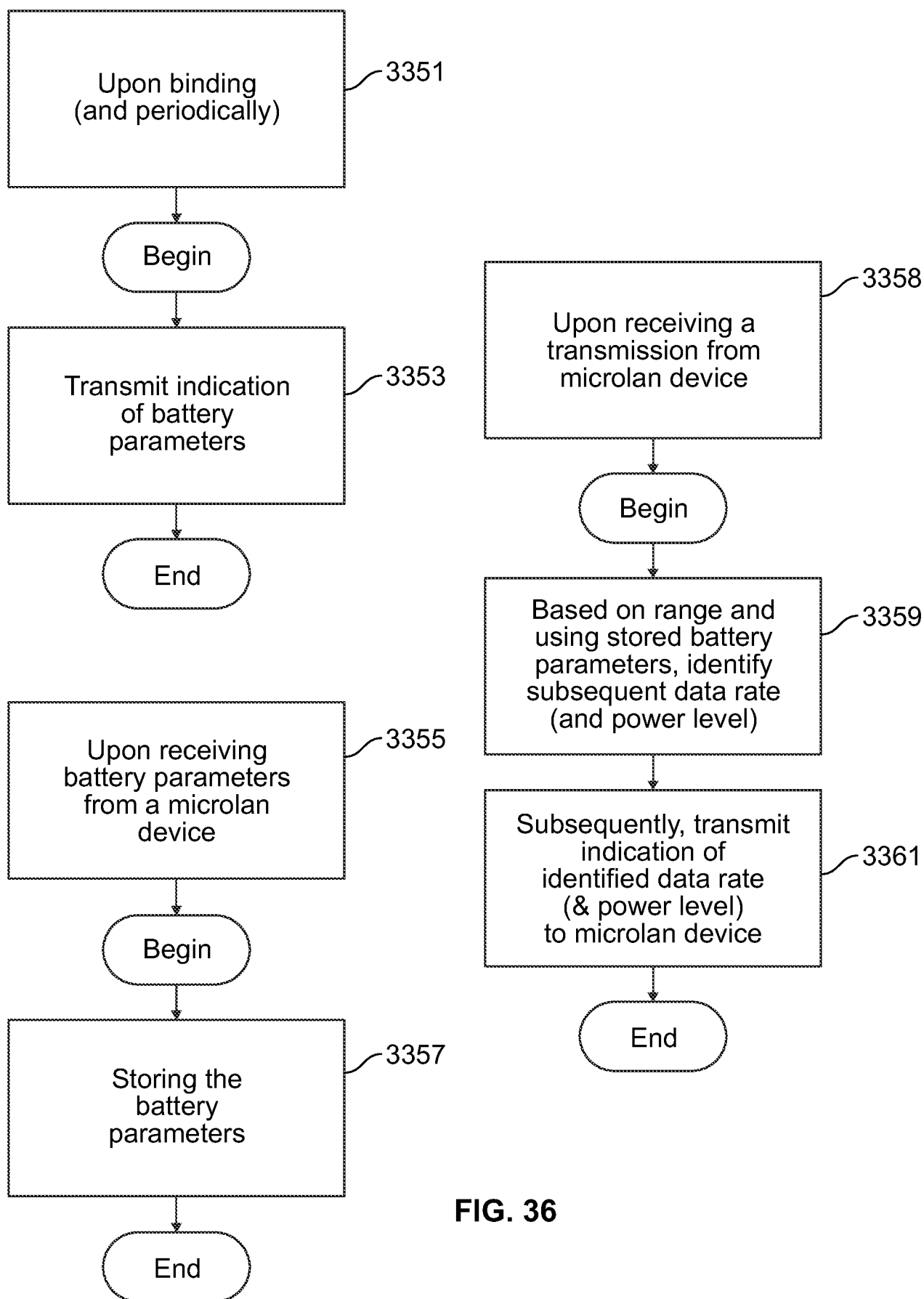


FIG. 36

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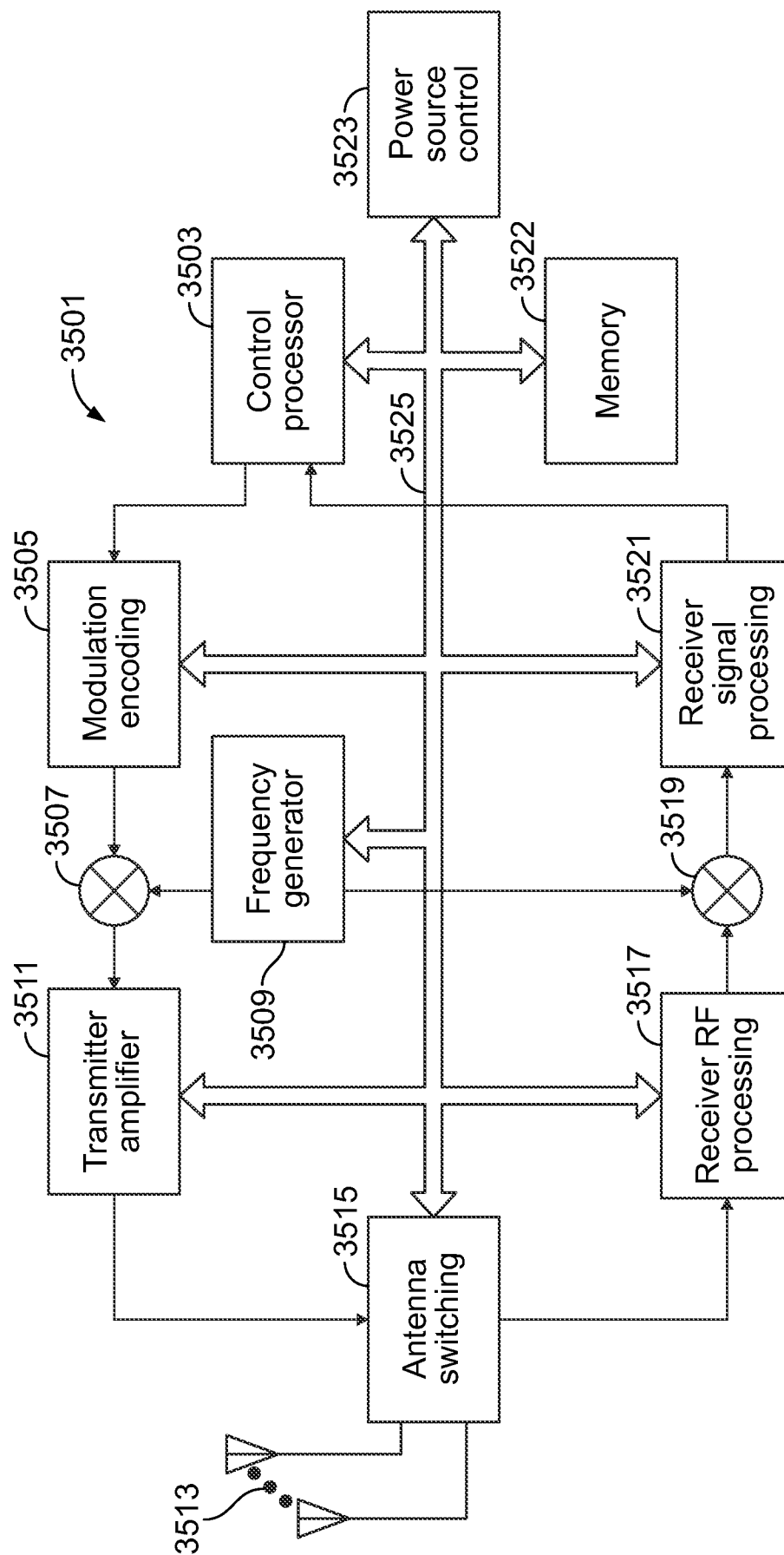


FIG. 37

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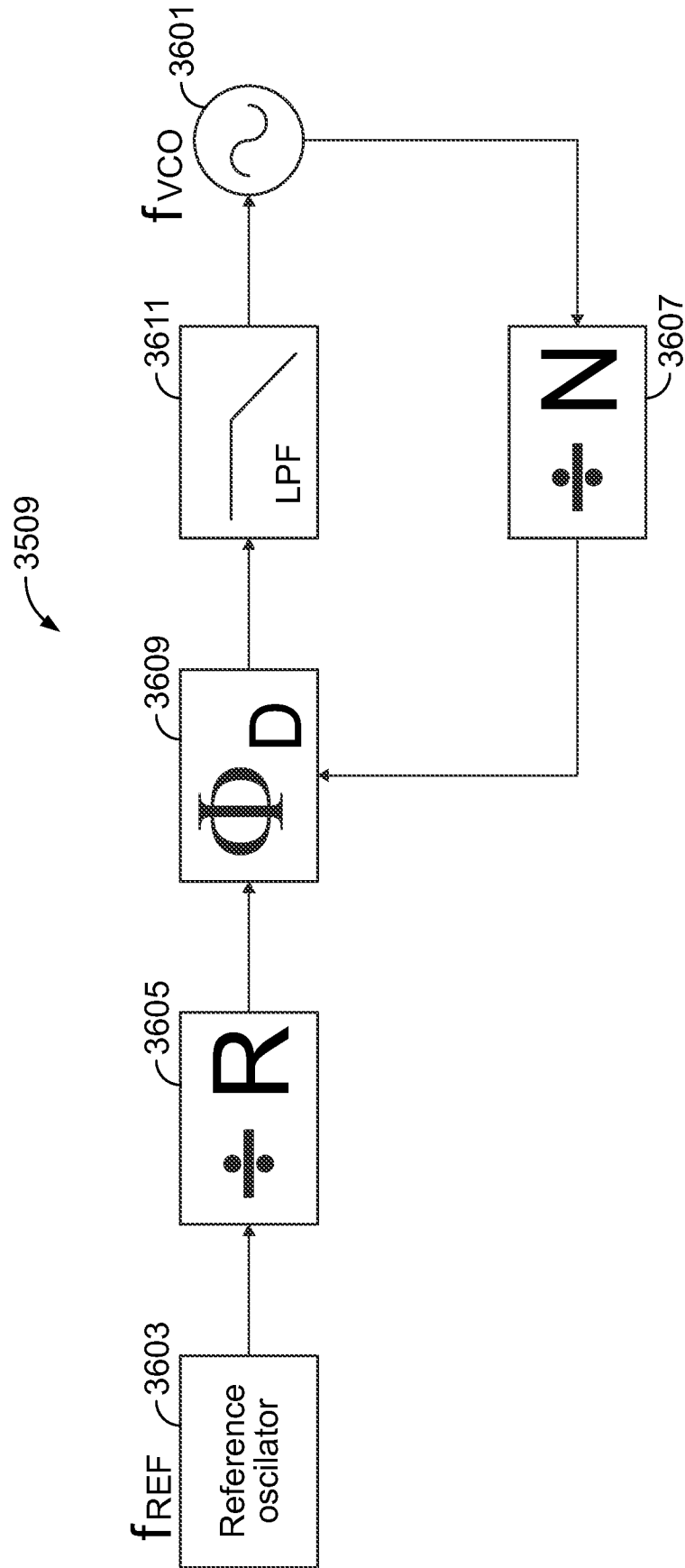


FIG. 38

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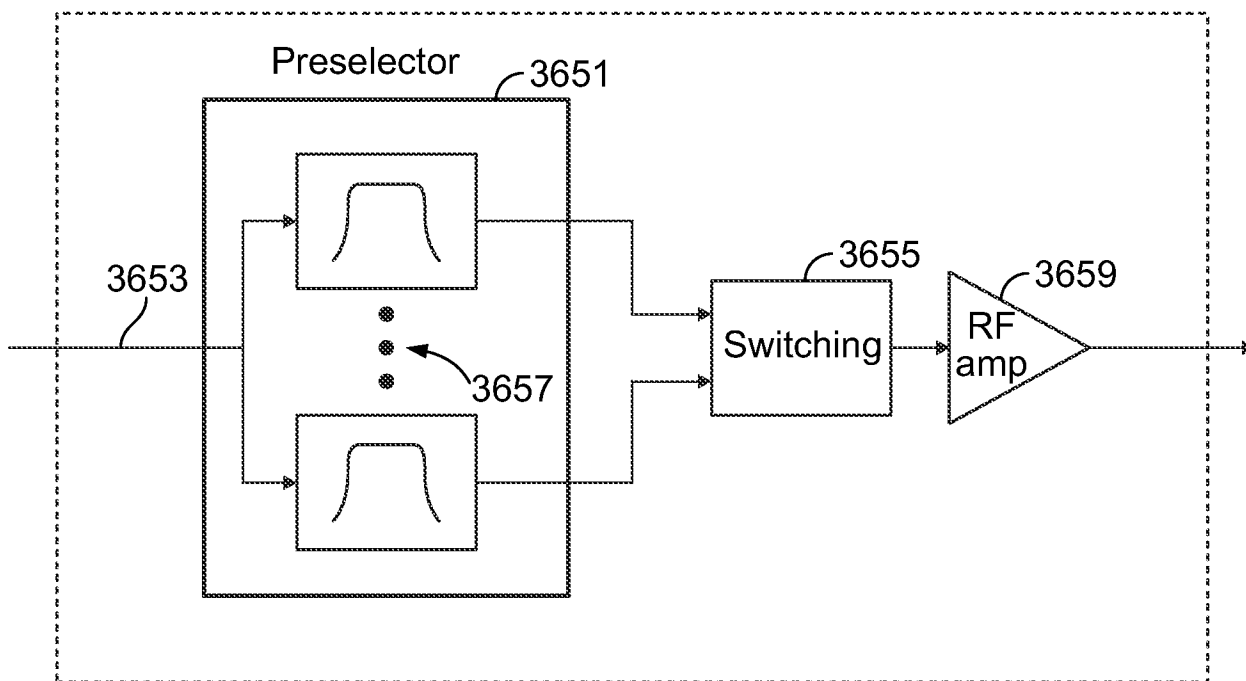


FIG. 39

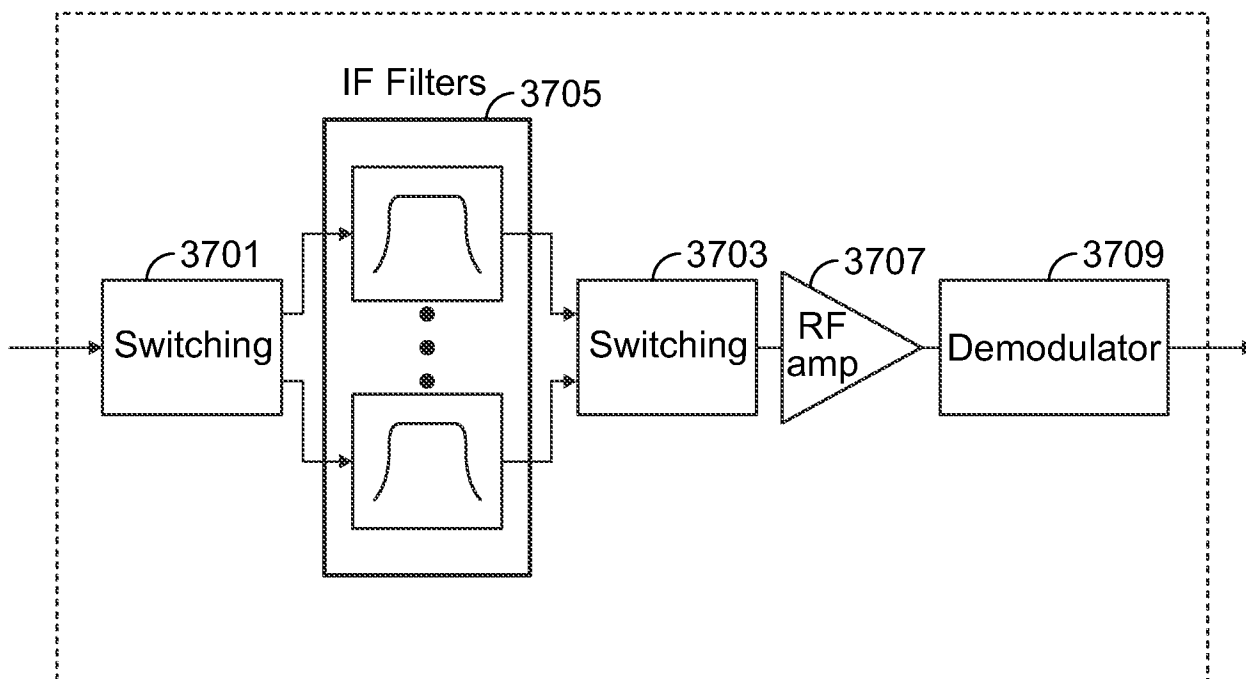


FIG. 40

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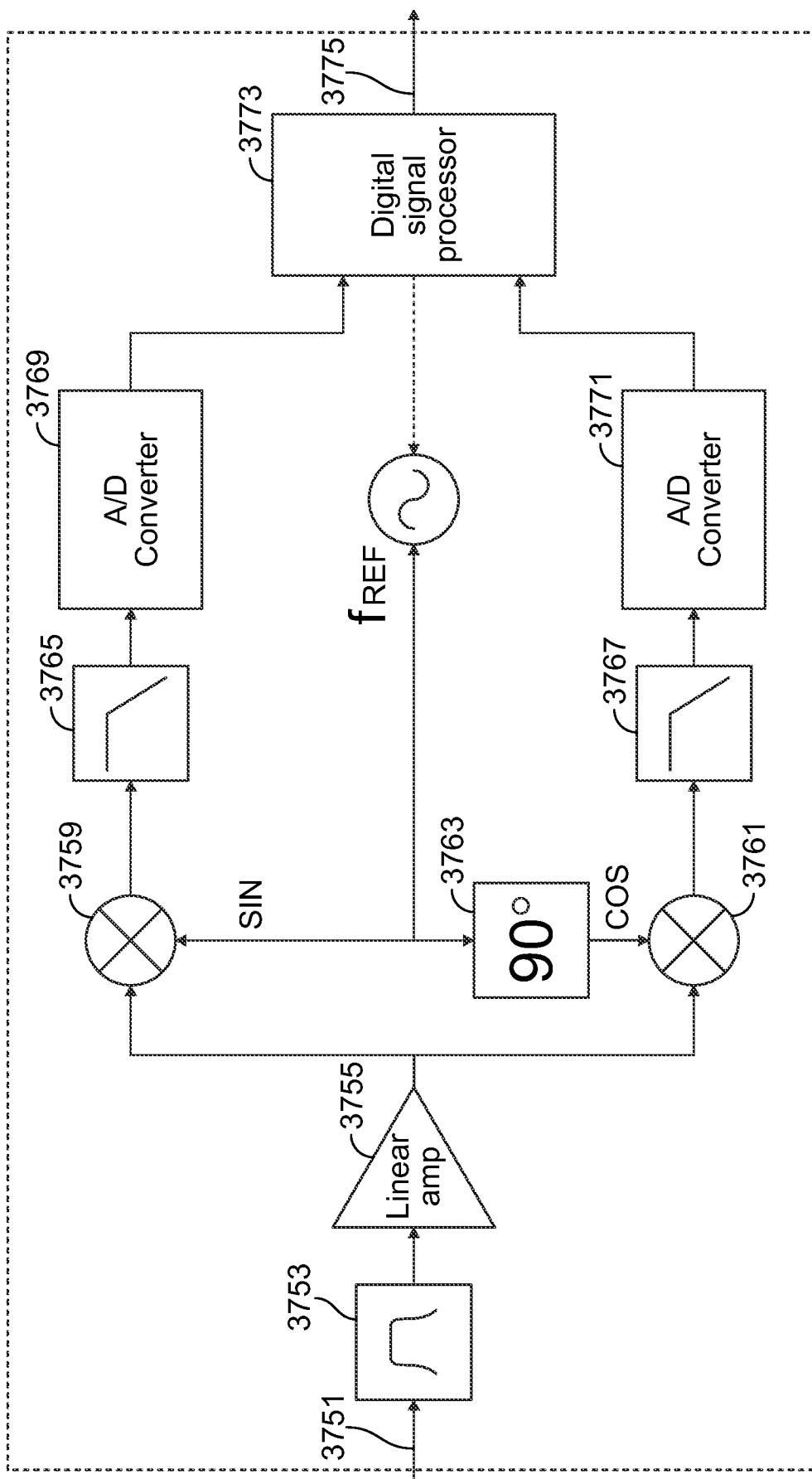


FIG. 41

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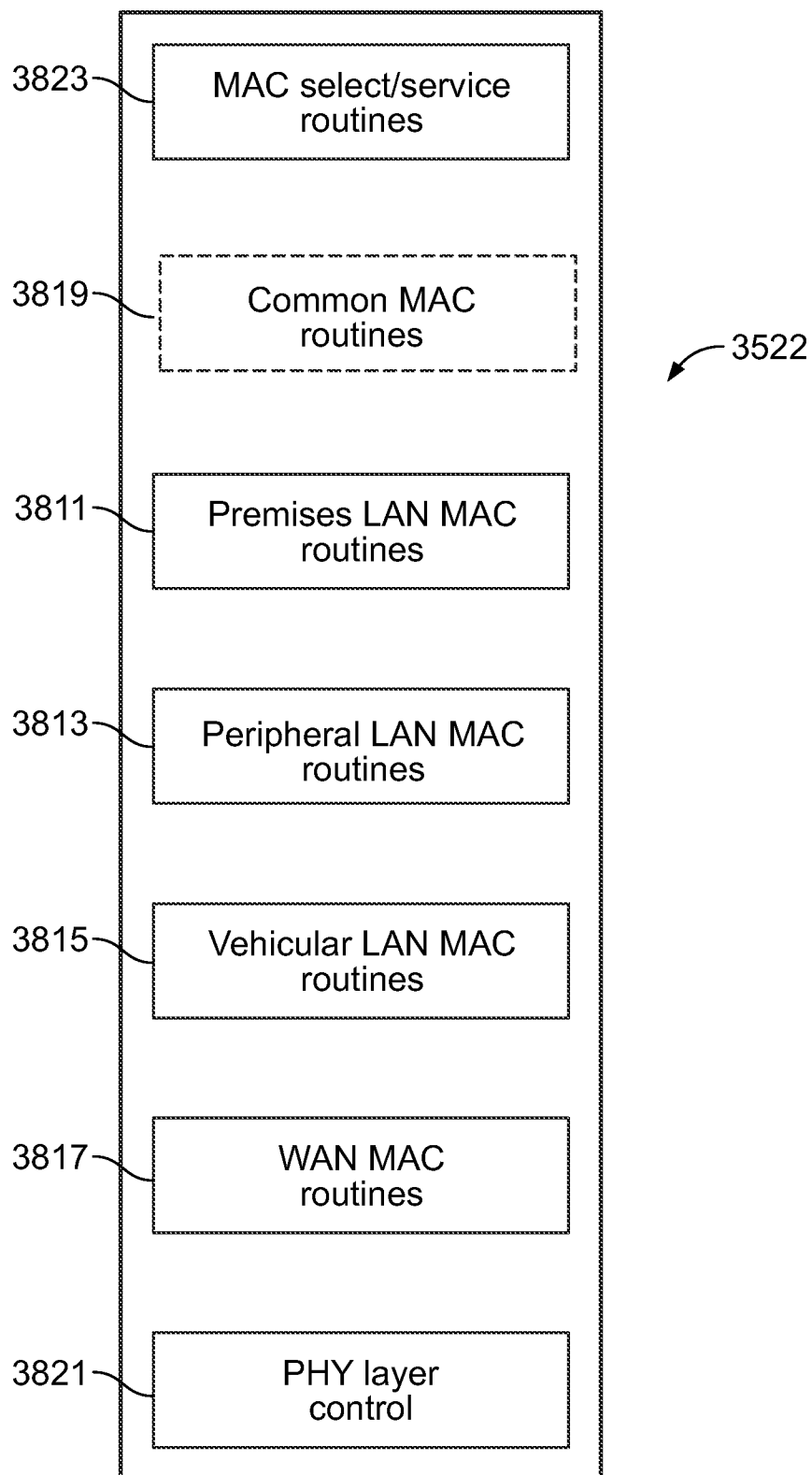


FIG. 42

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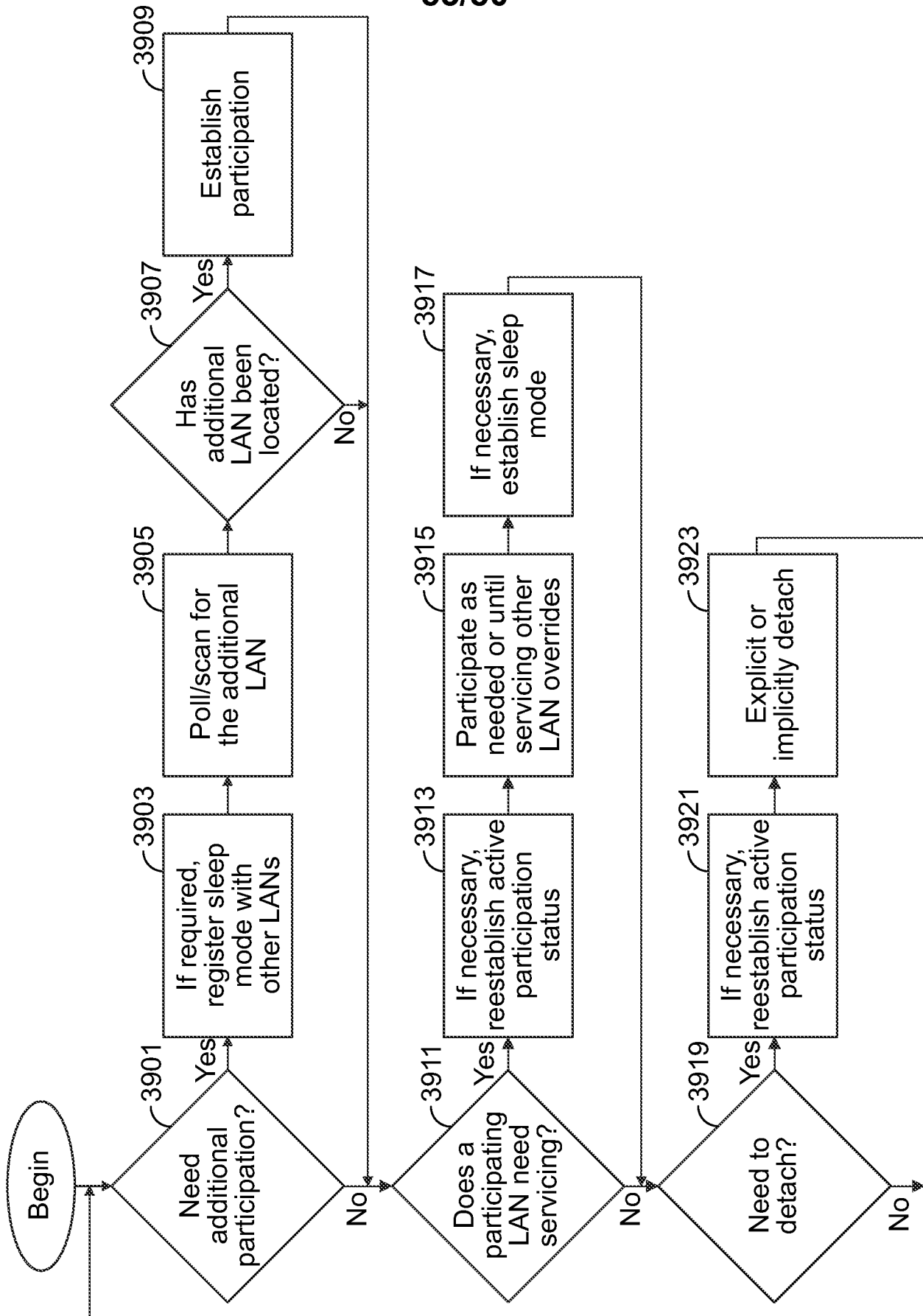


FIG. 43

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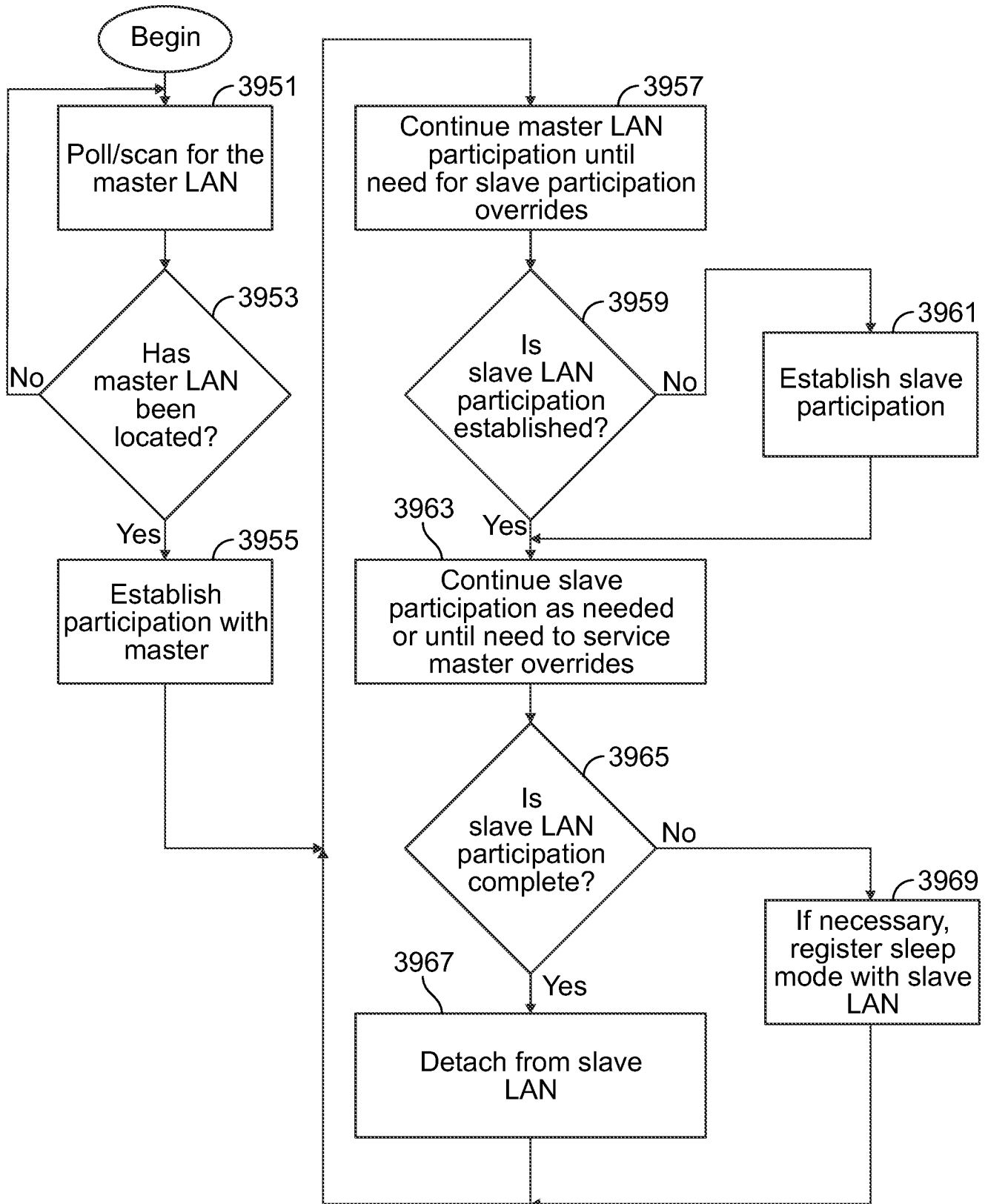


FIG. 44

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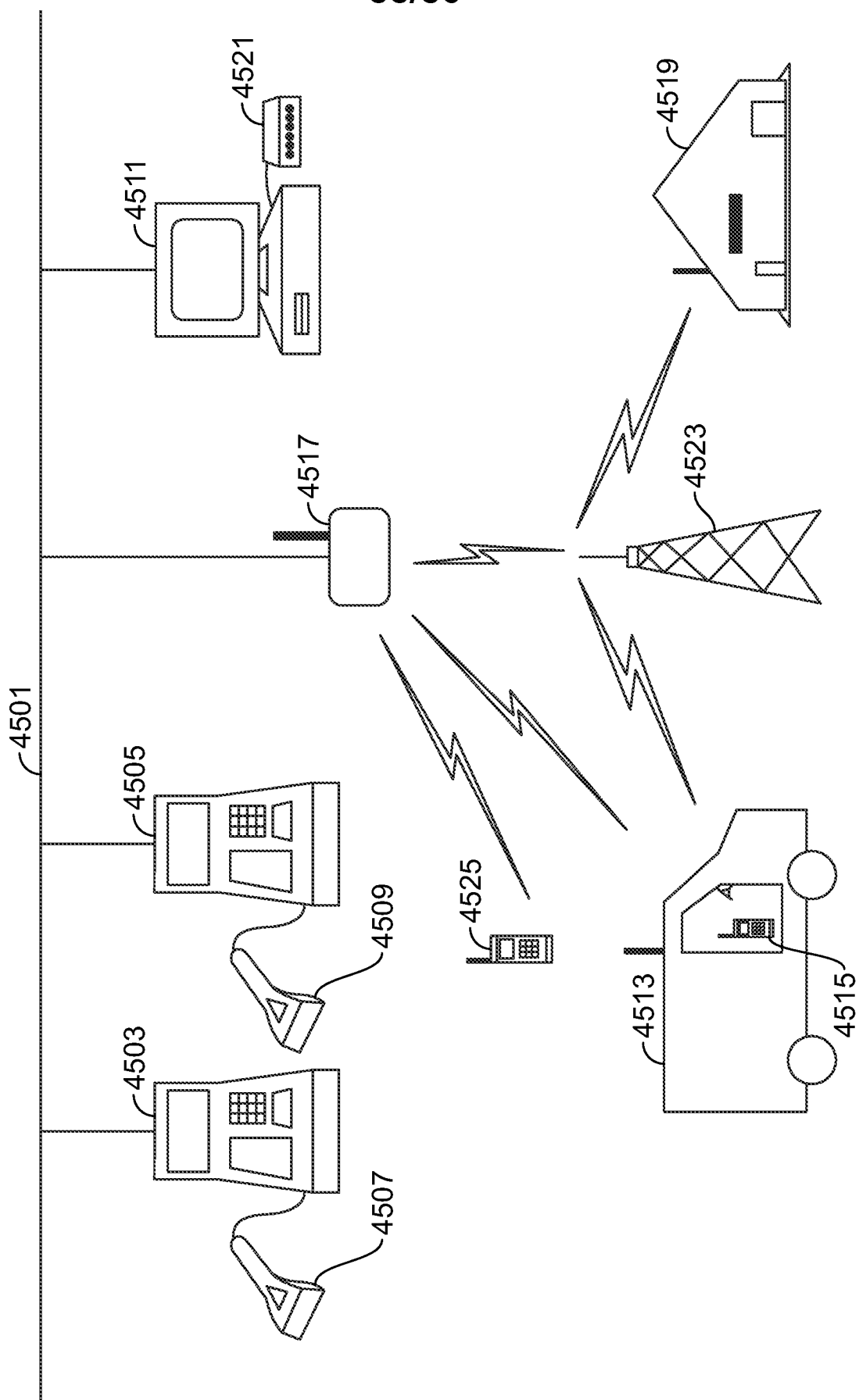


FIG. 45

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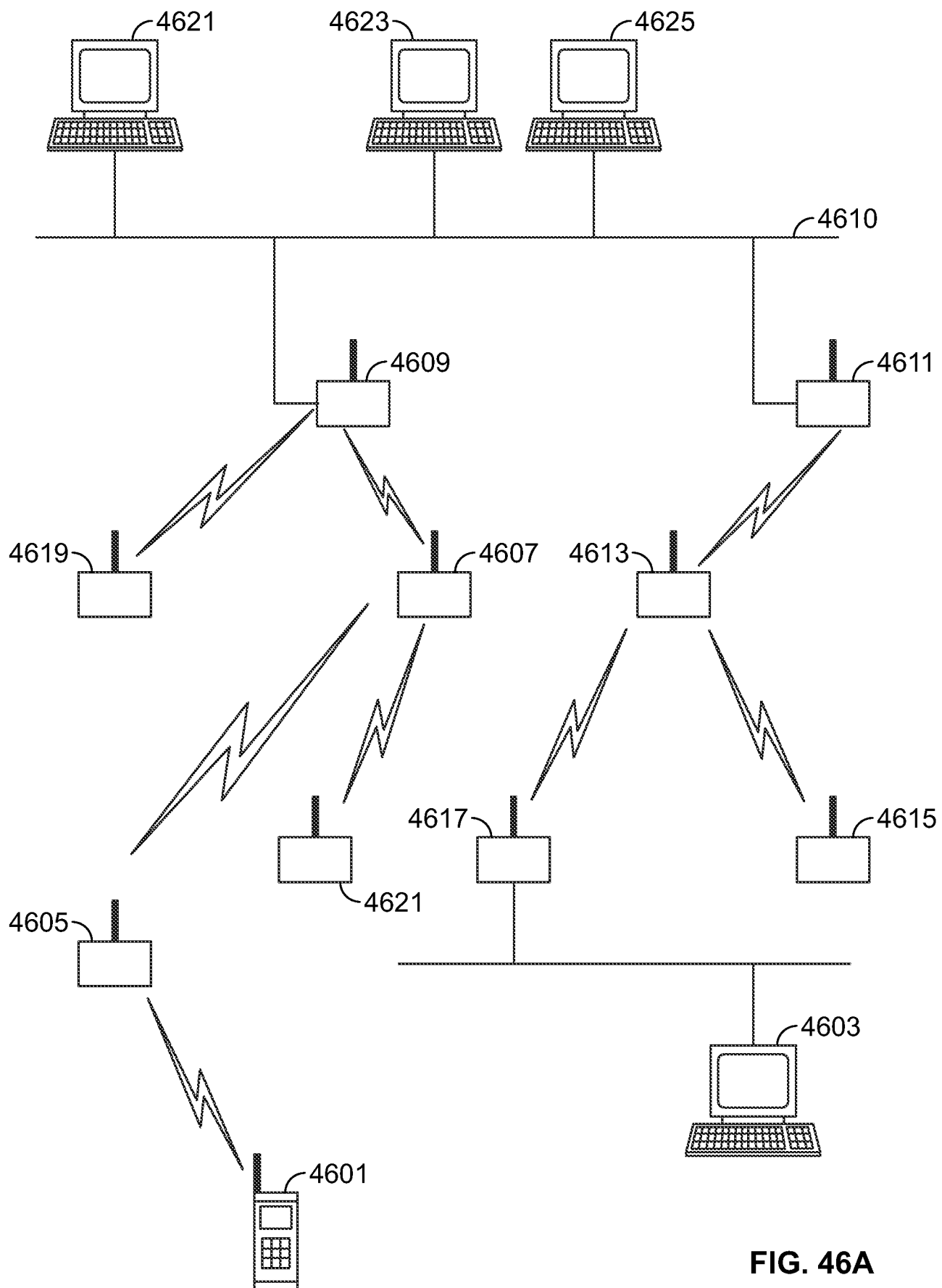


FIG. 46A

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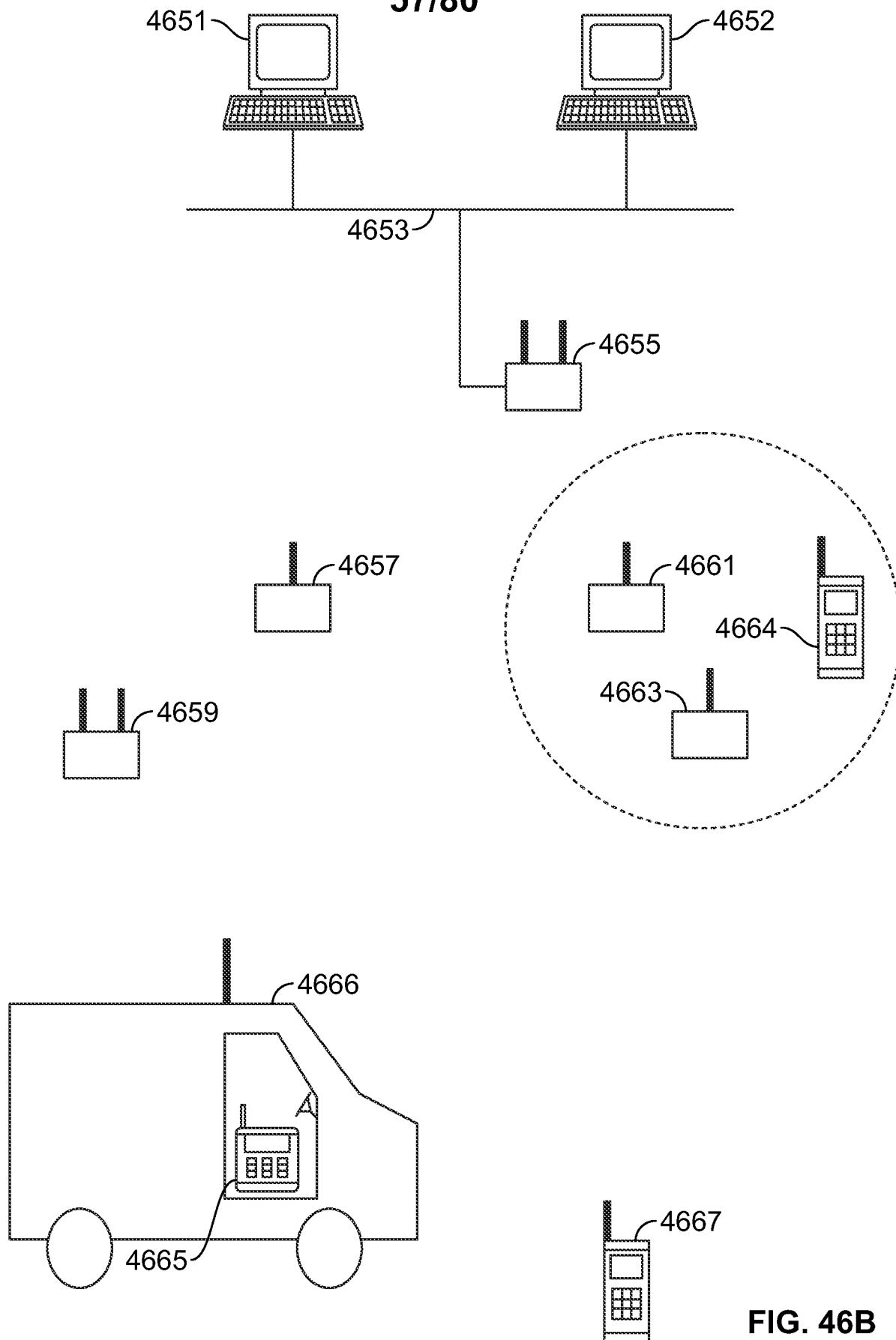


FIG. 46B

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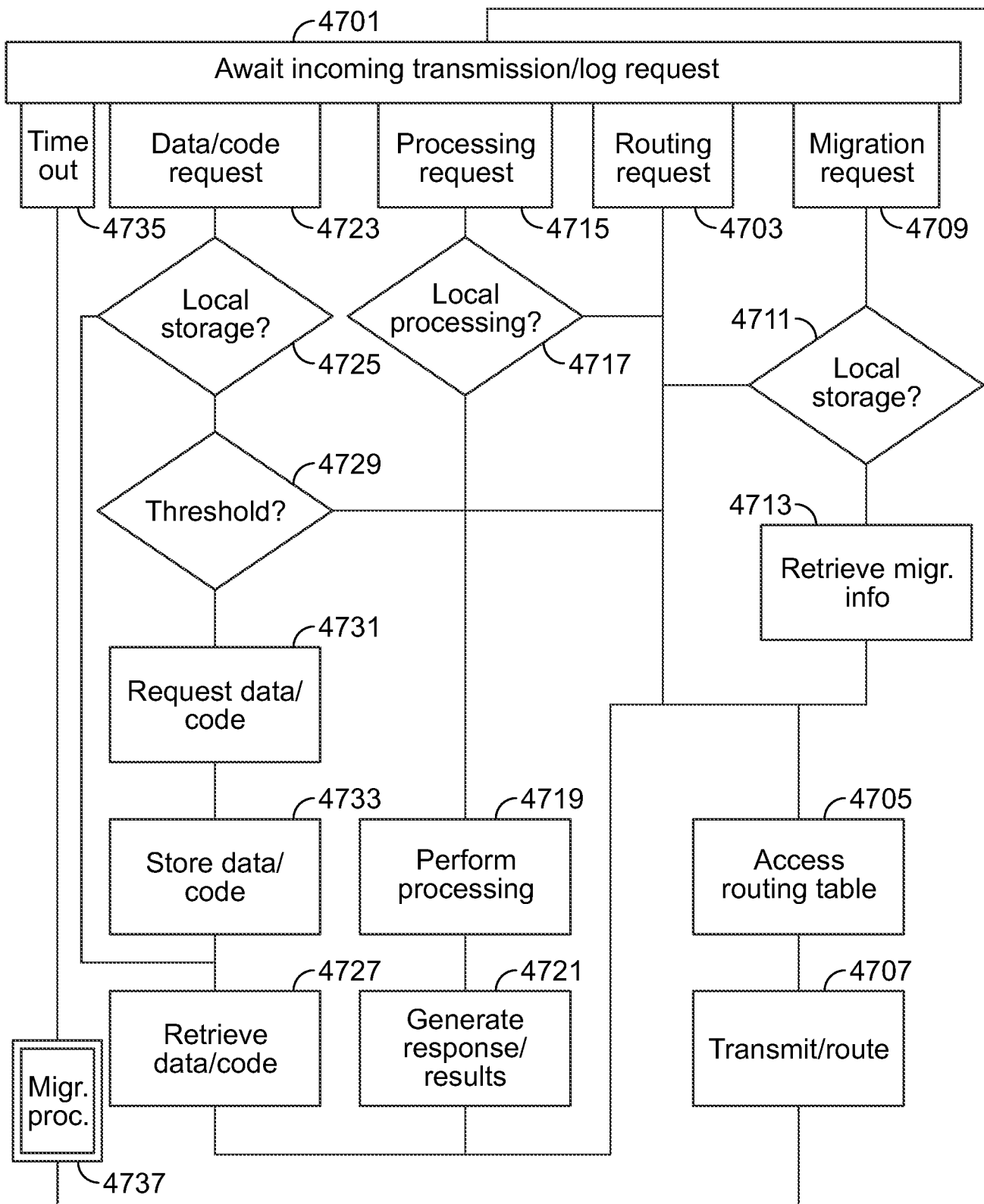


FIG. 47A

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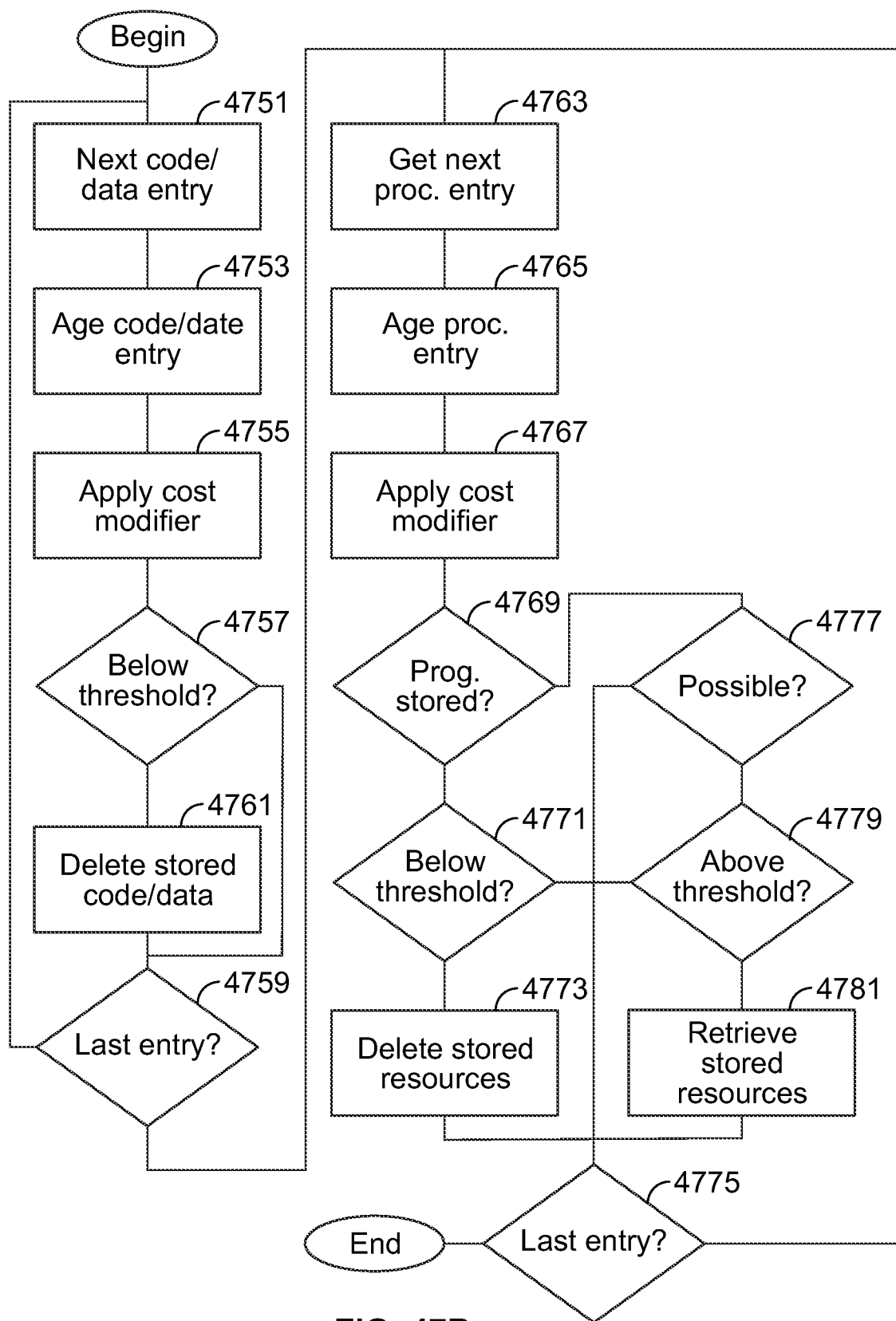


FIG. 47B

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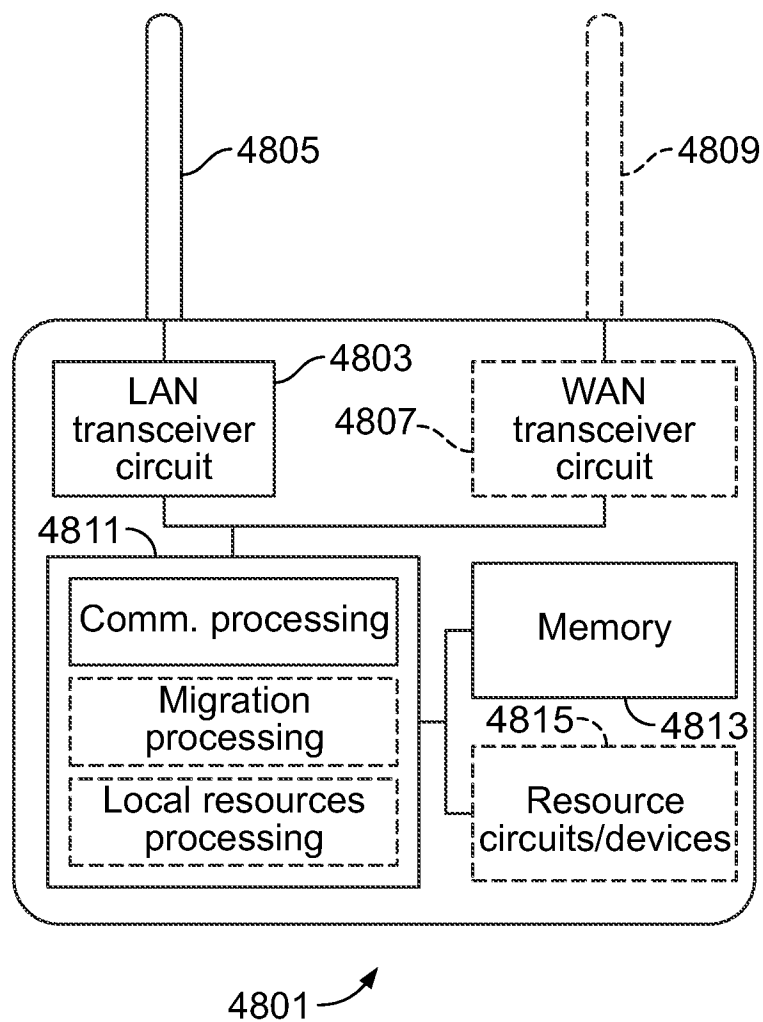


FIG. 48

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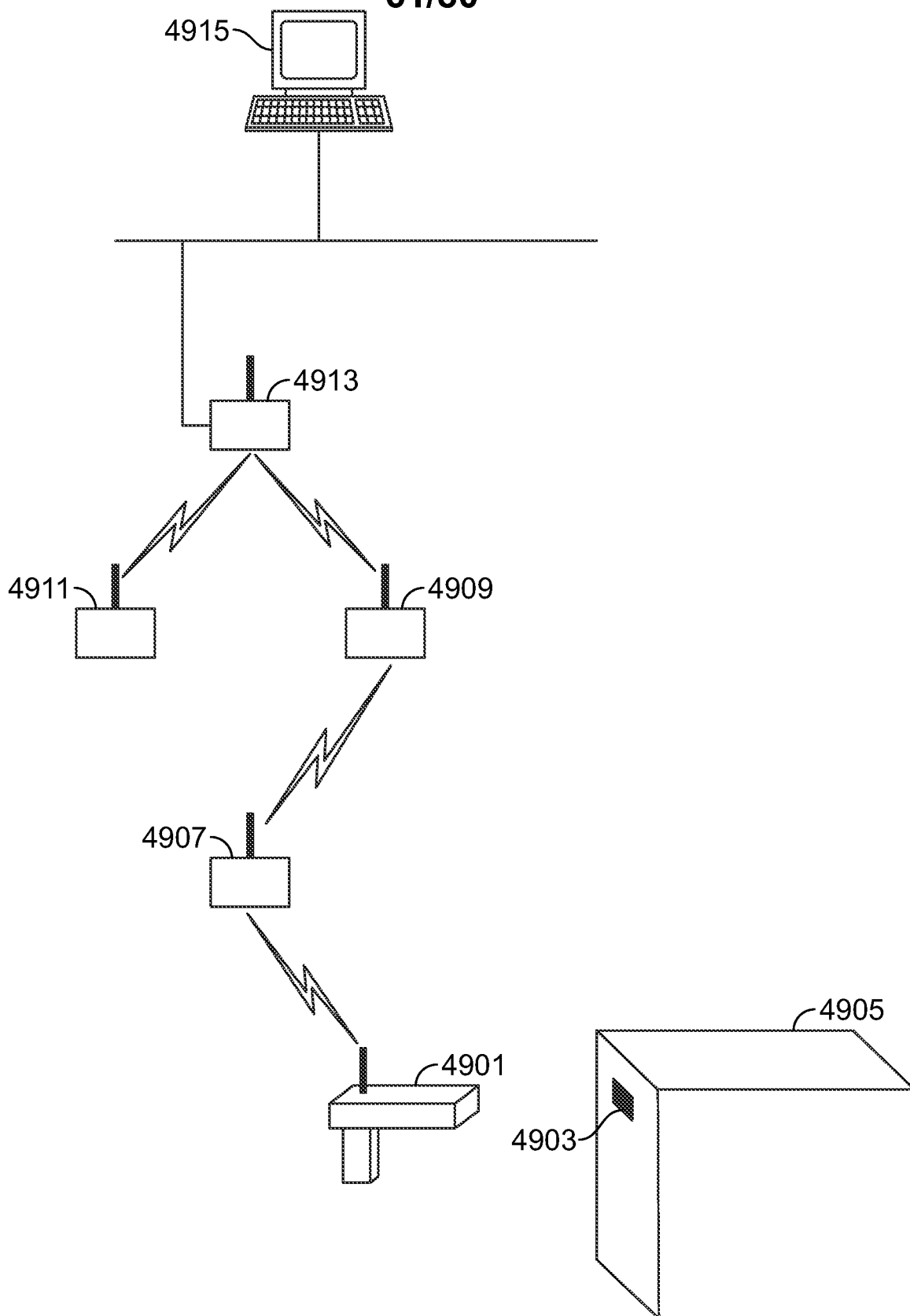


FIG. 49A

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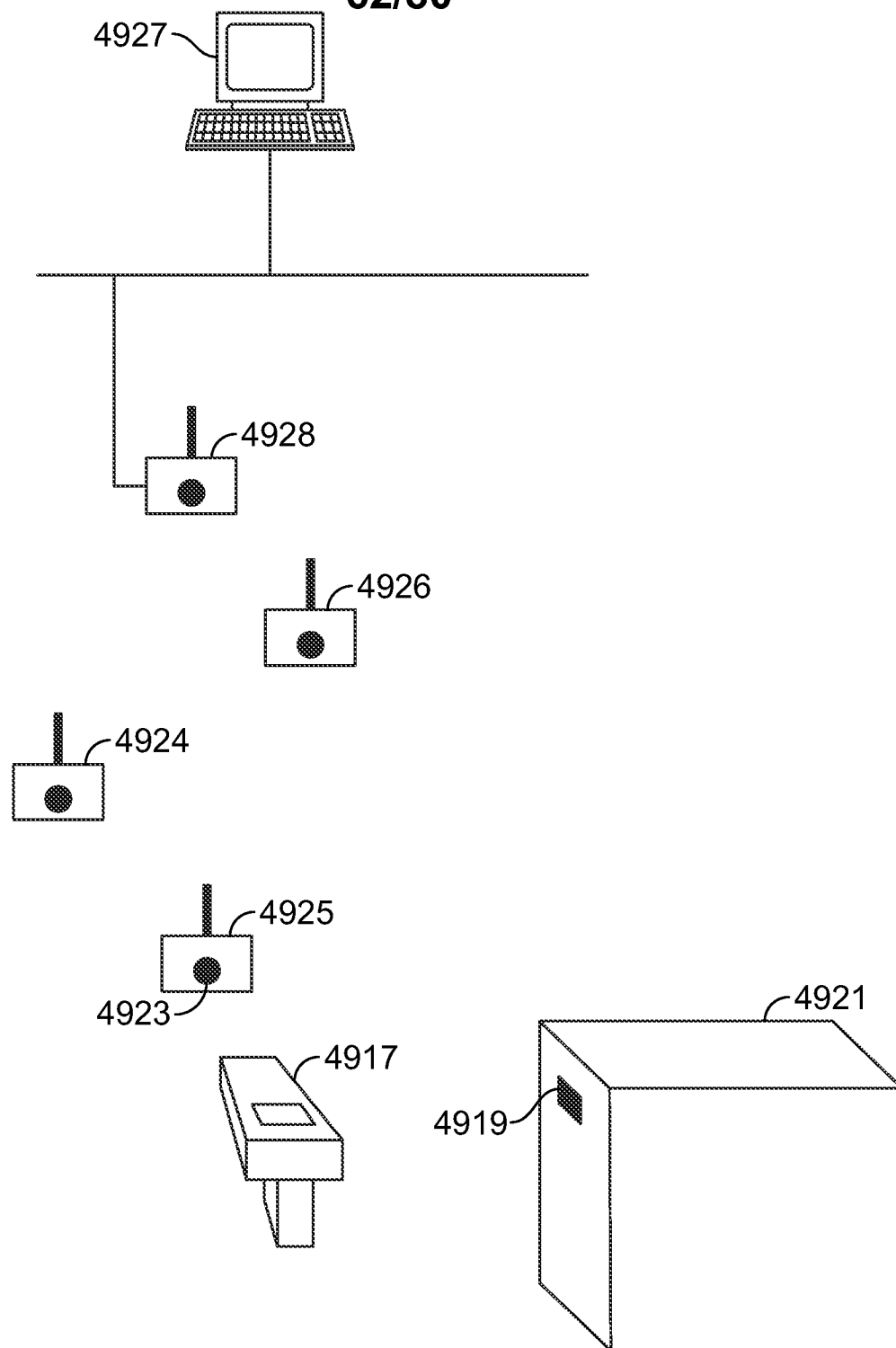


FIG. 49B

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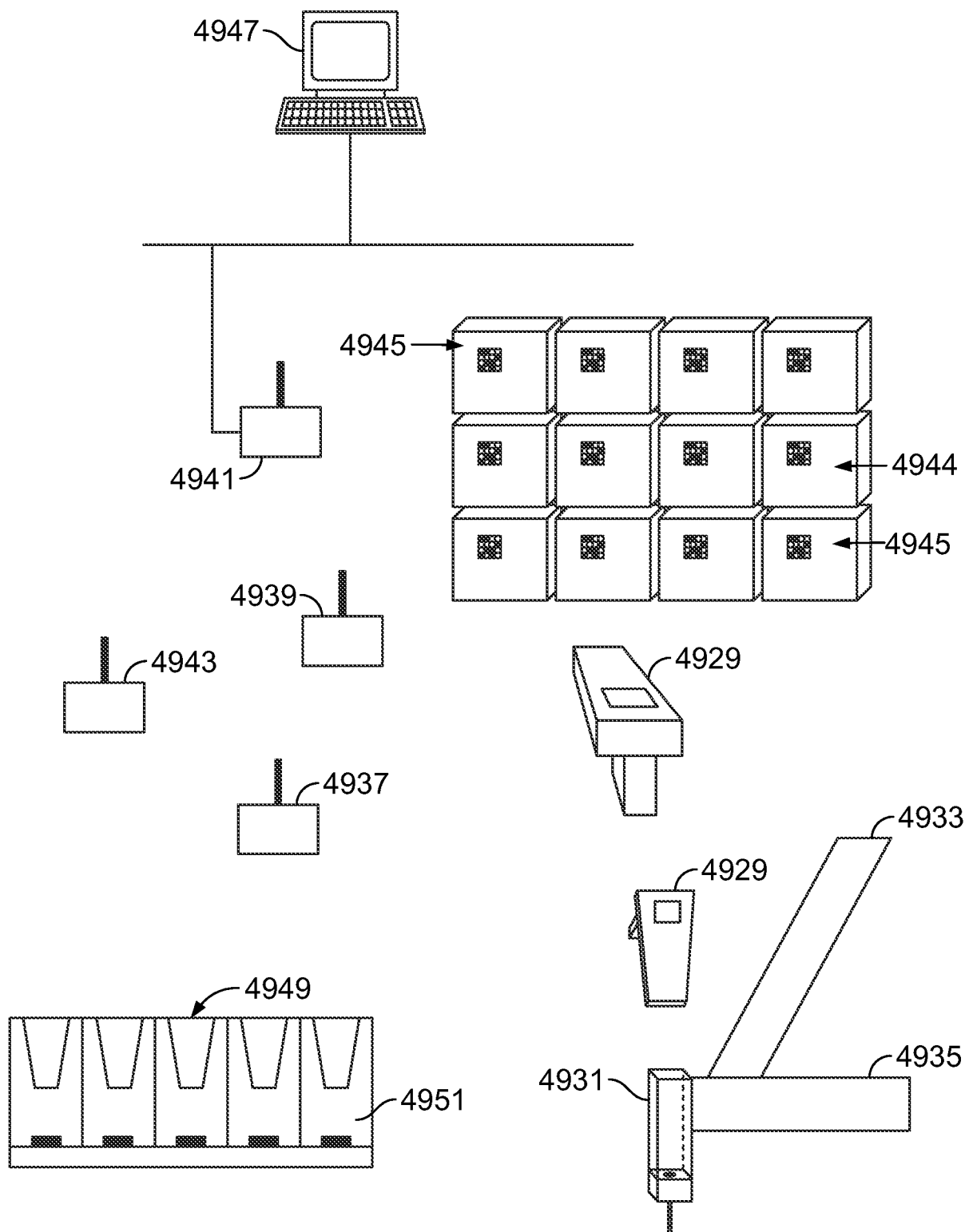


FIG. 49C

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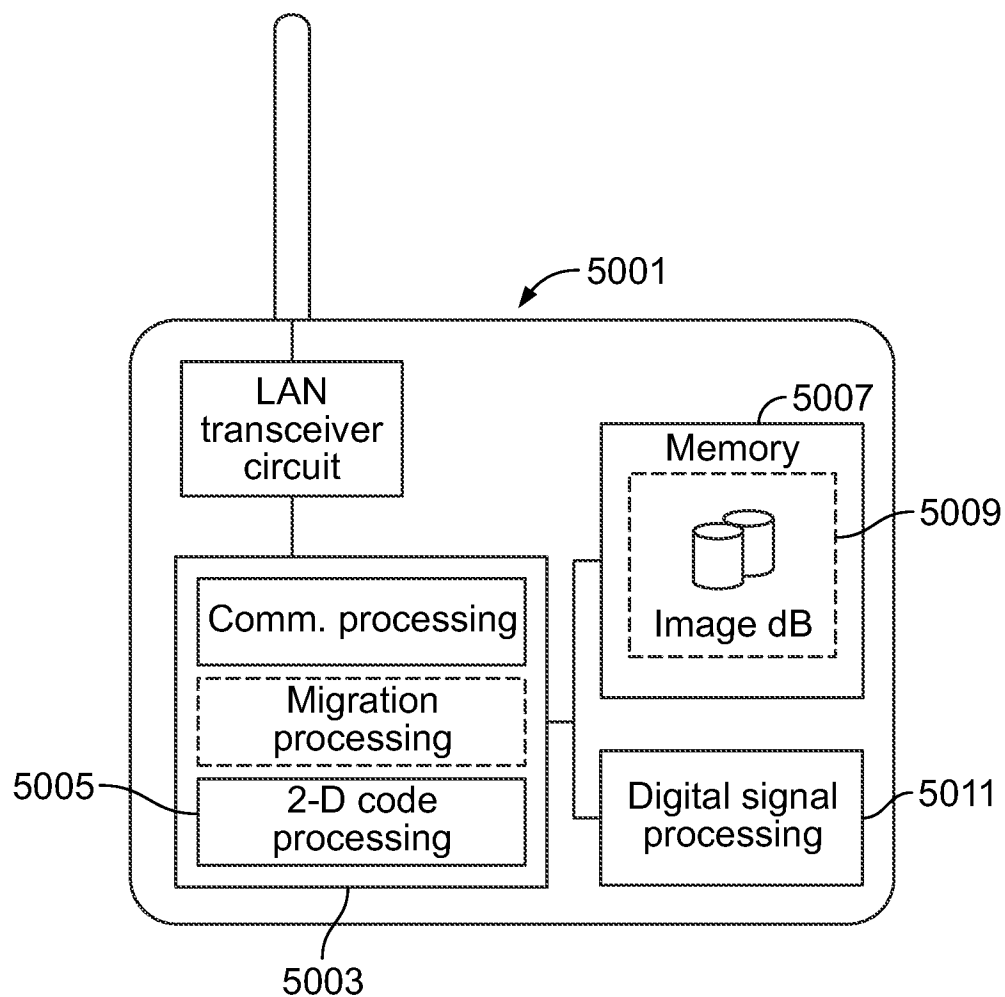


FIG. 50

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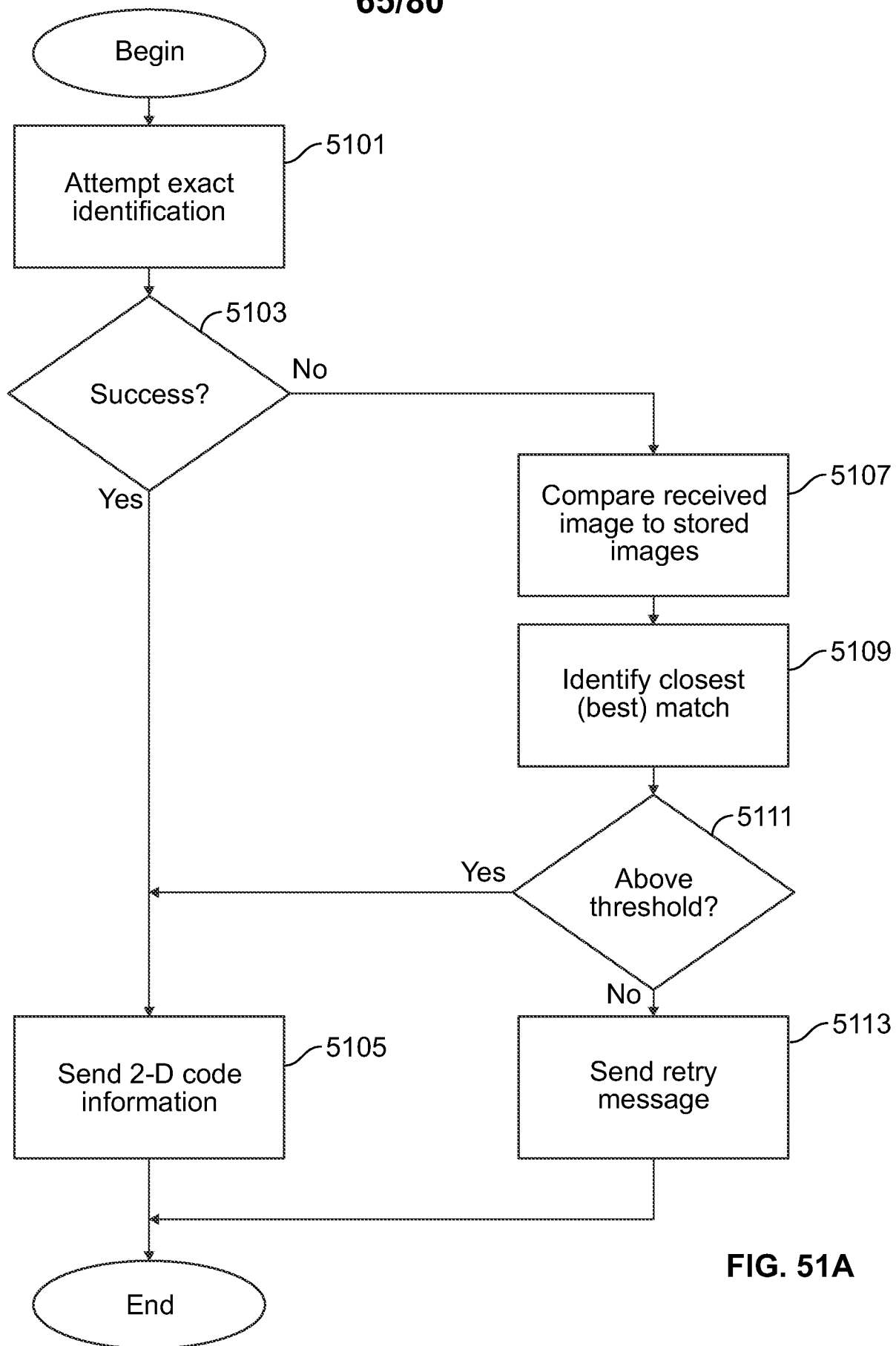


FIG. 51A

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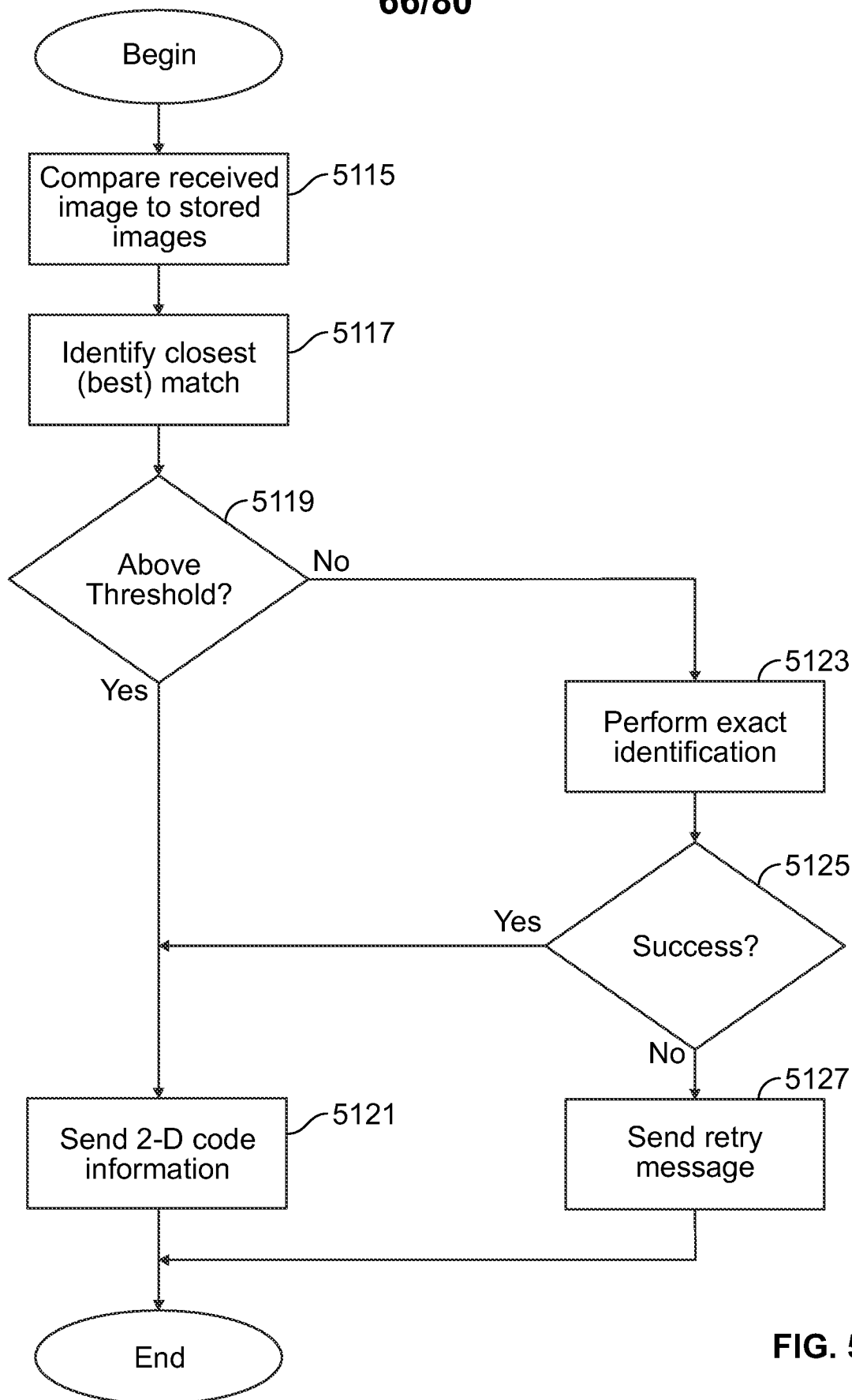


FIG. 51B

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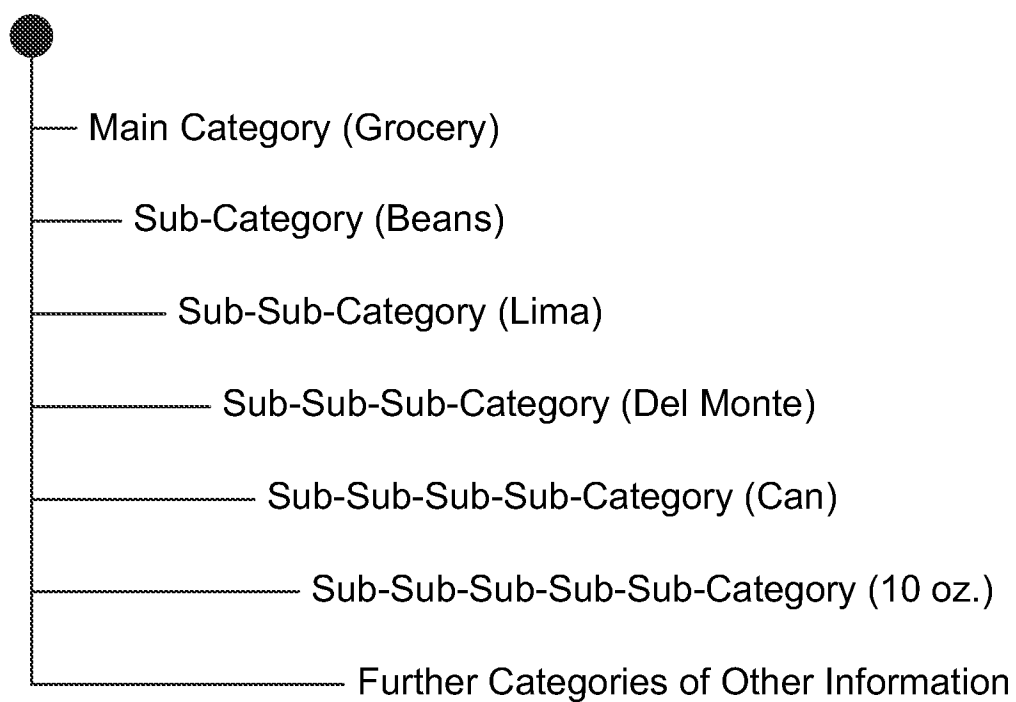


FIG. 52

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Grocery	Beans	Lima
Del Monte	Can	10 oz.
Other Information		

5303

5301

FIG. 53

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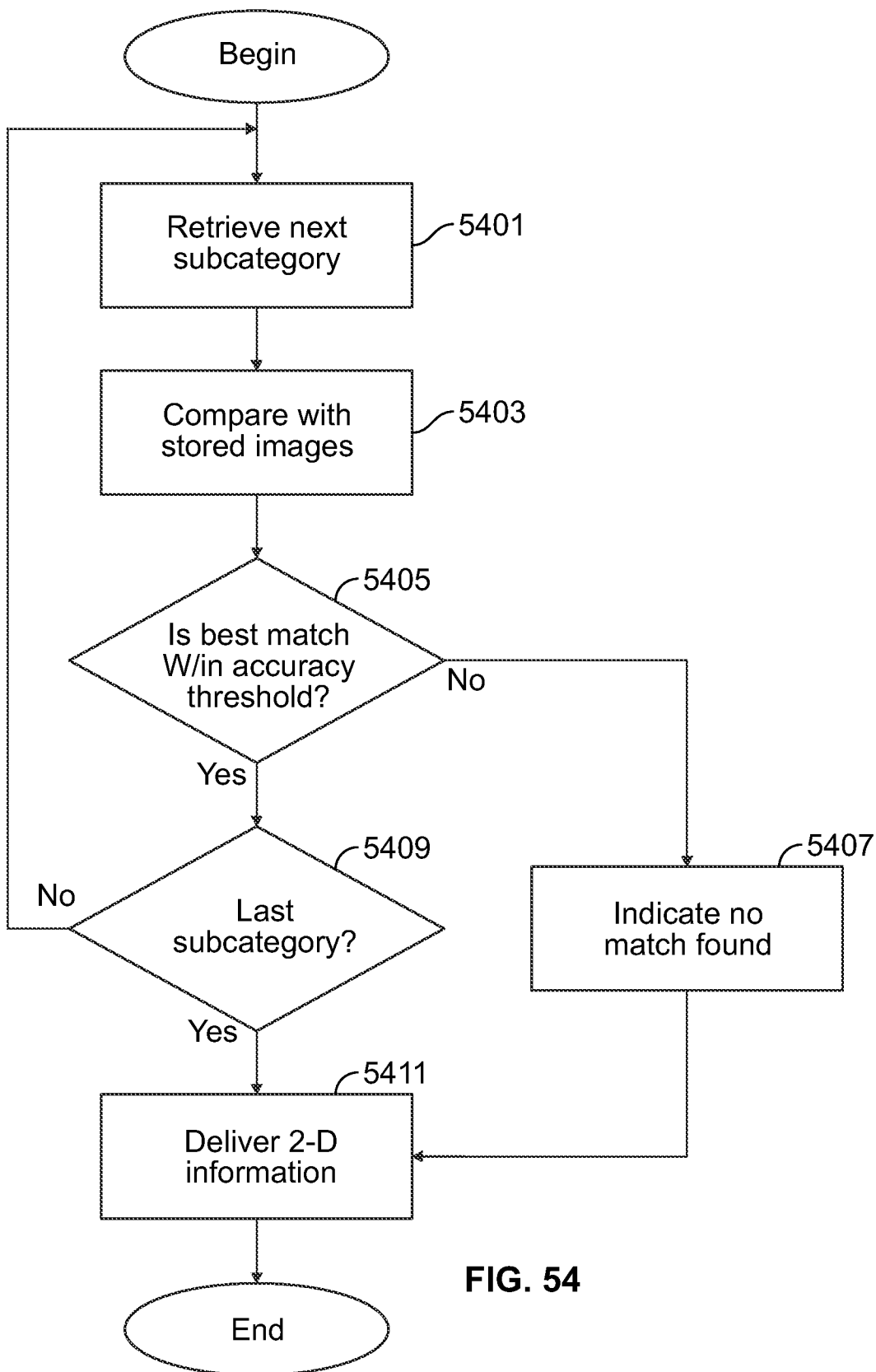


FIG. 54

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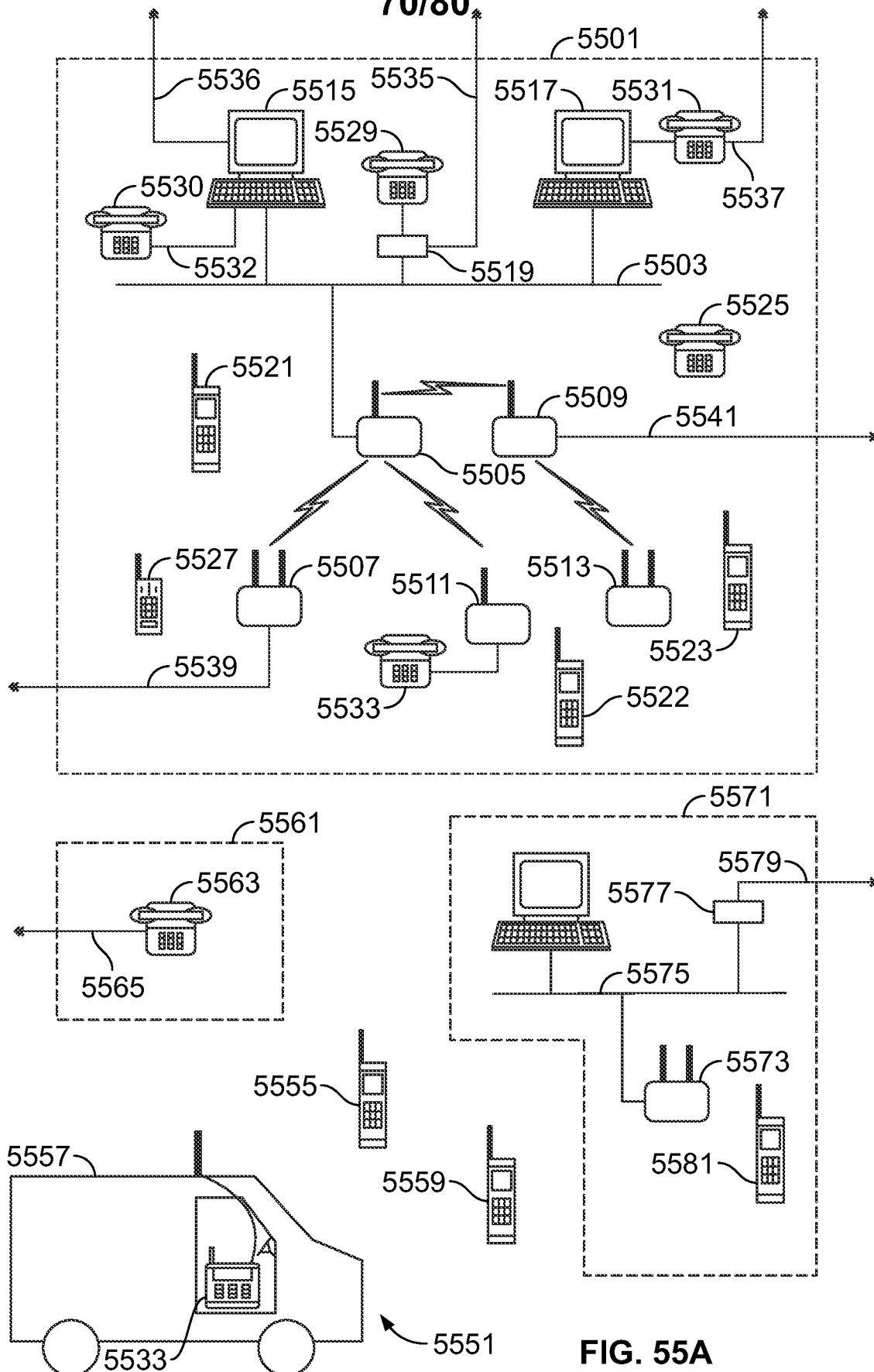


FIG. 55A

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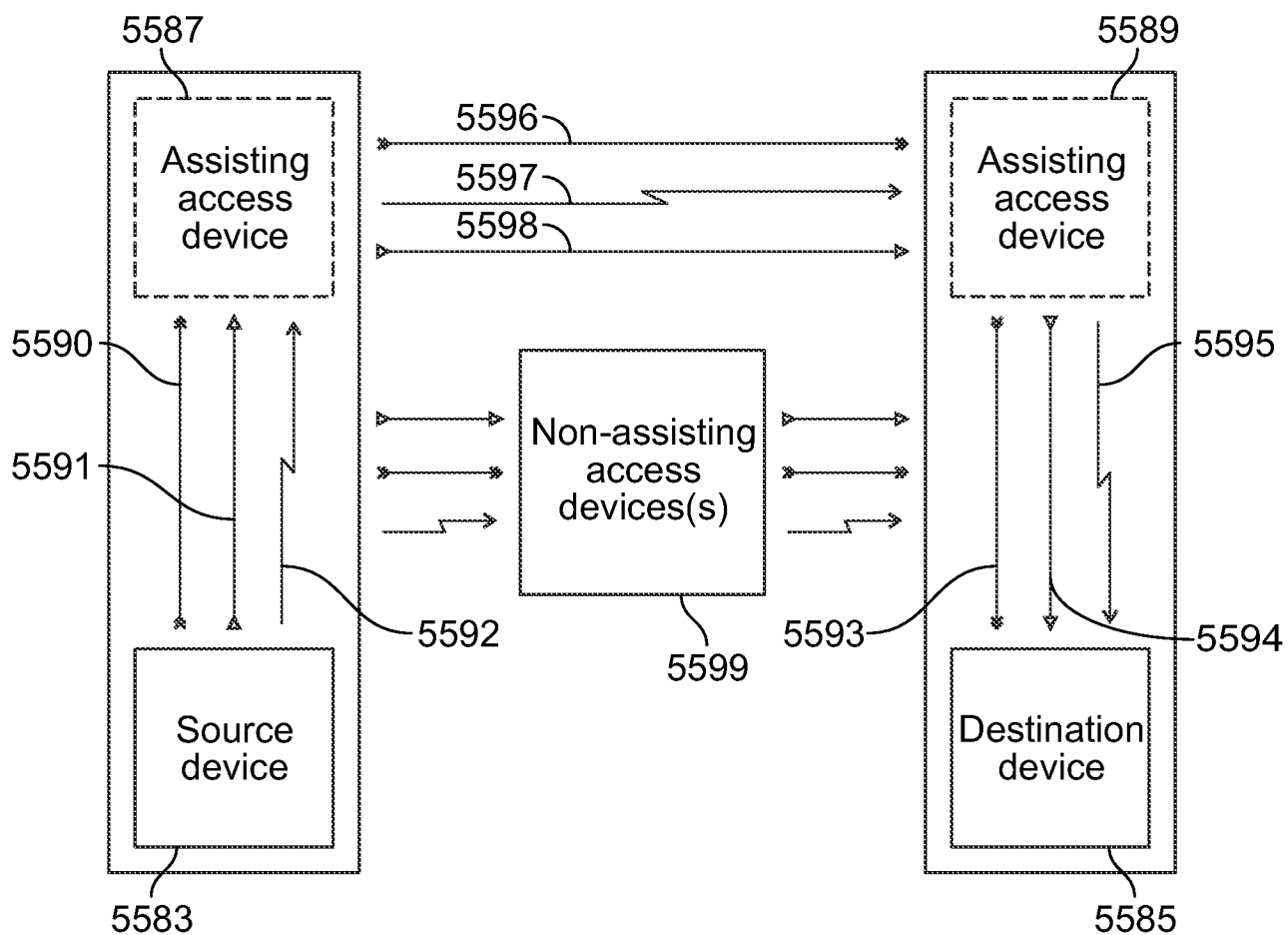


FIG. 55B

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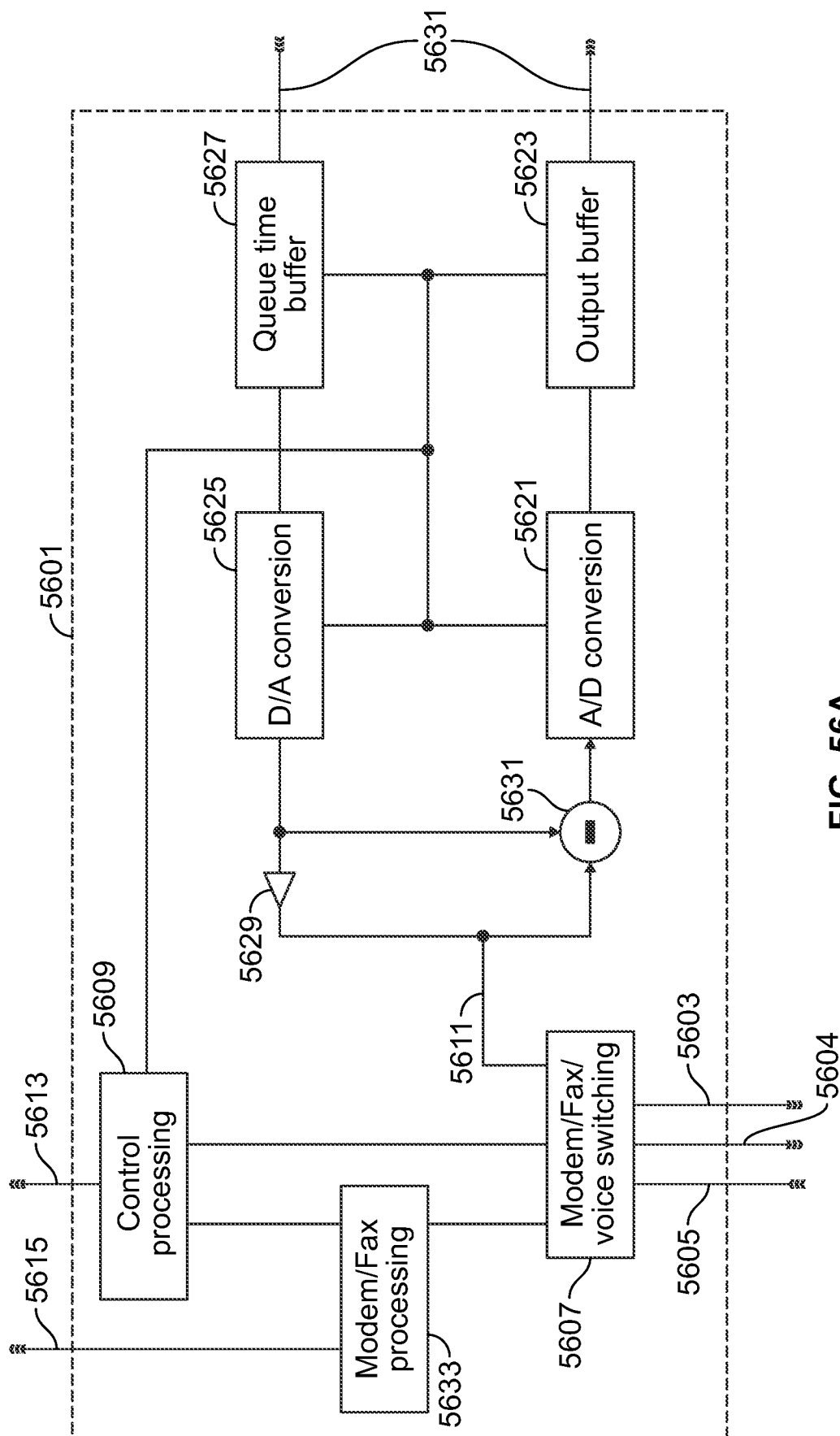


FIG. 56A

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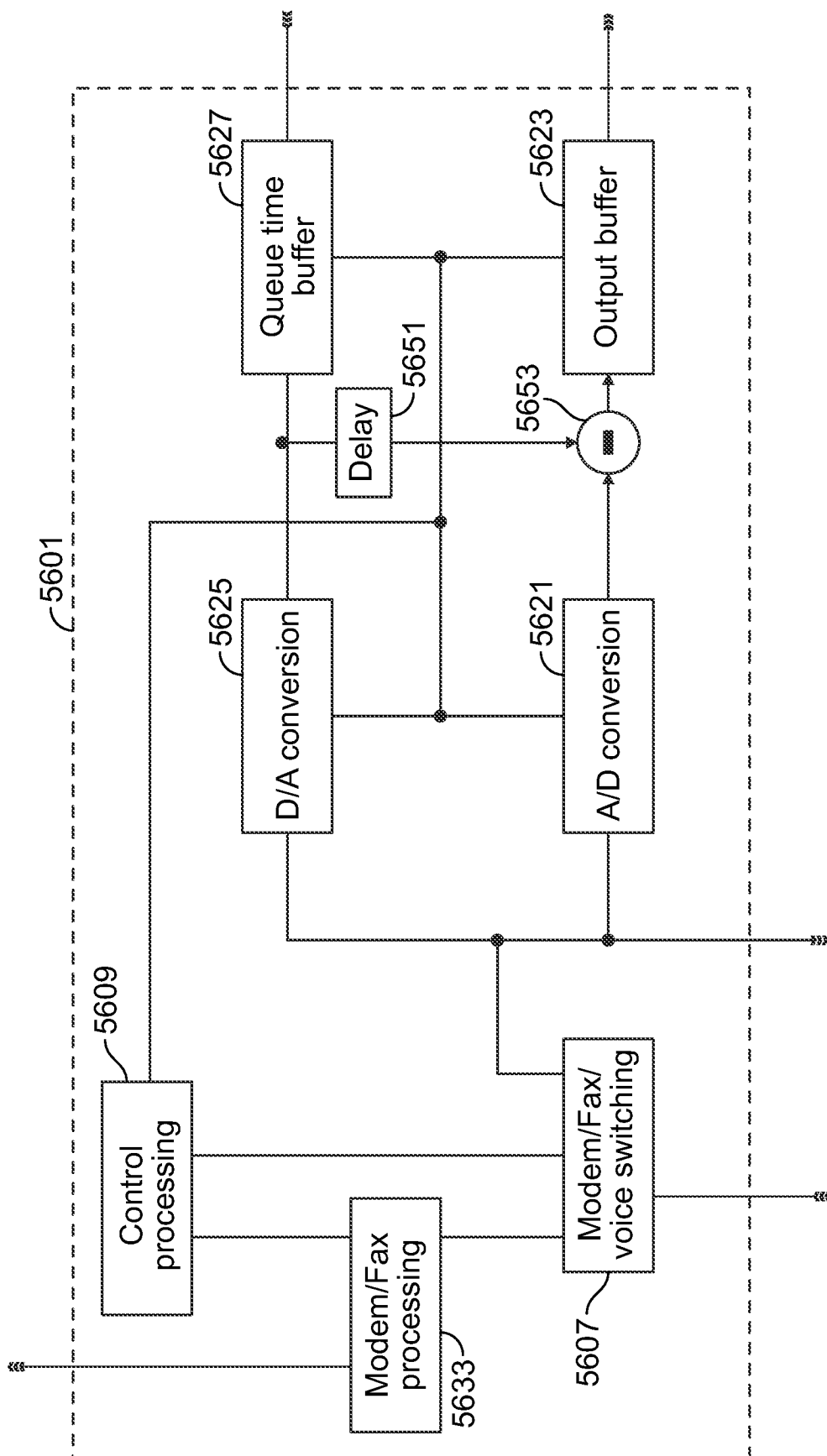


FIG. 56B

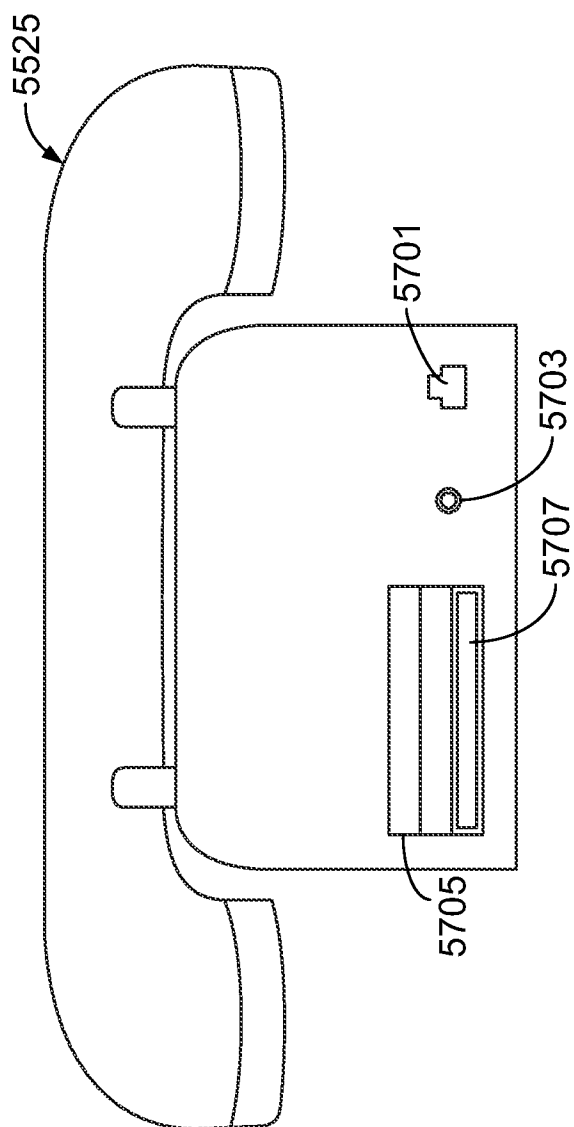


FIG. 57

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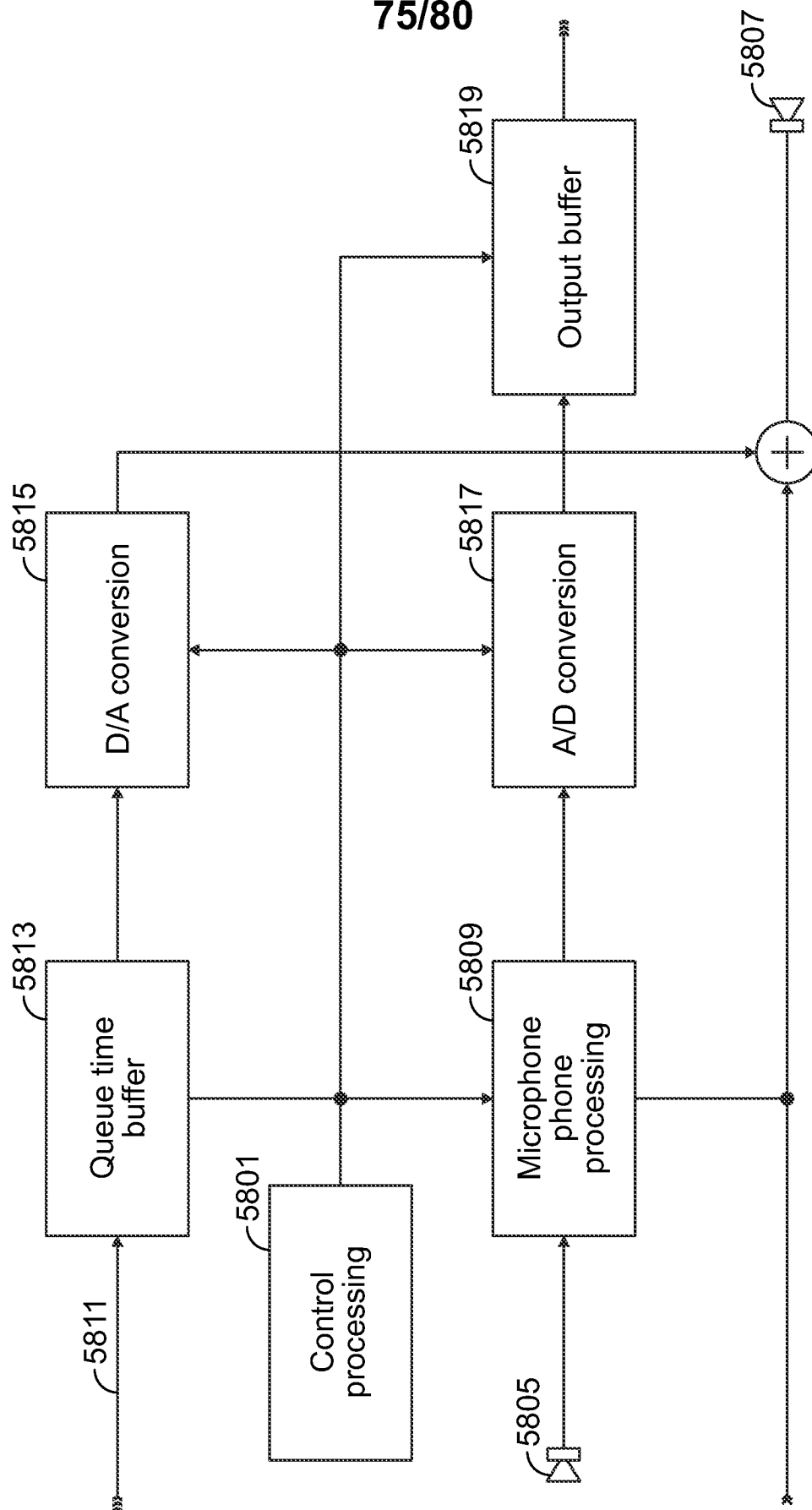


FIG. 58

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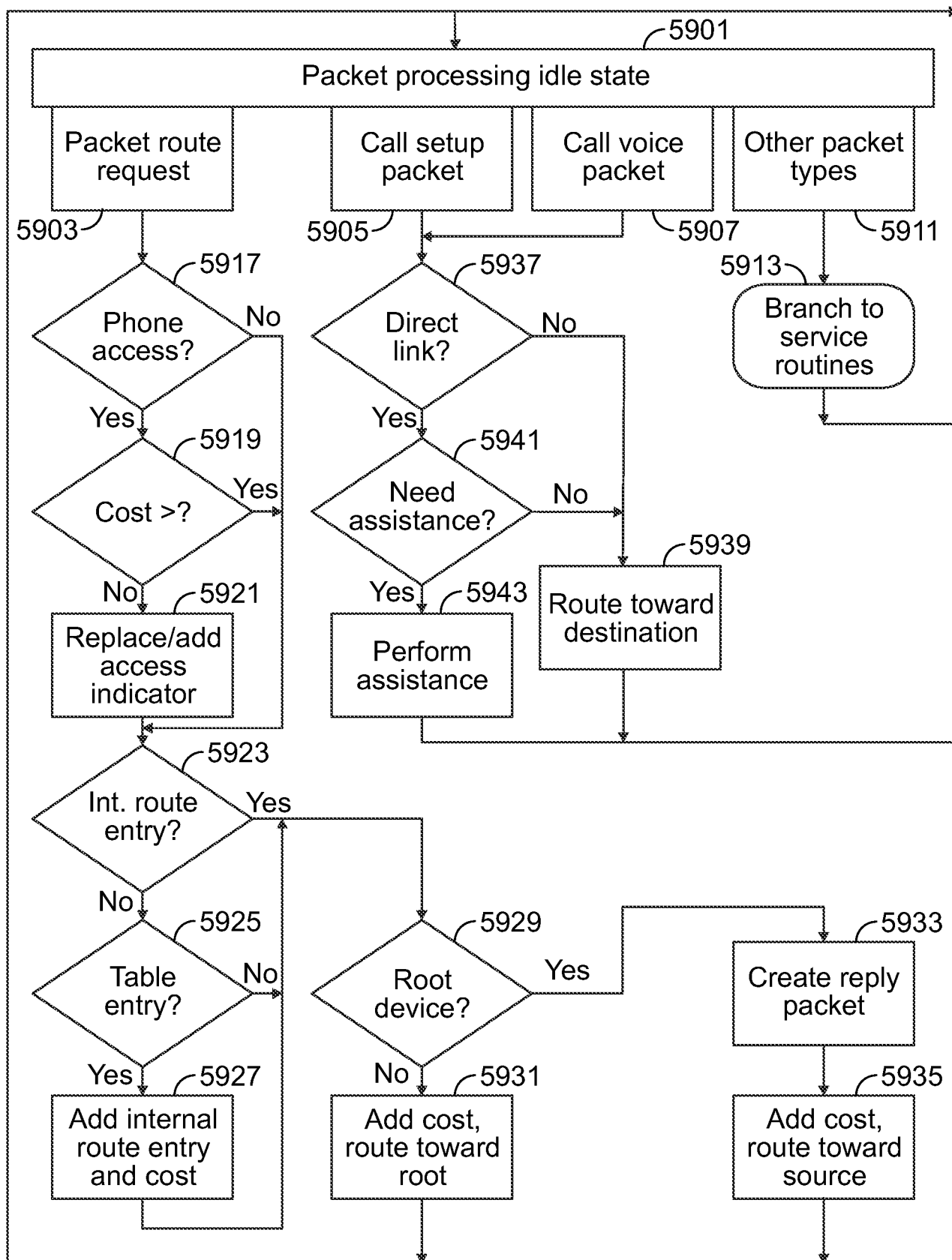


FIG. 59

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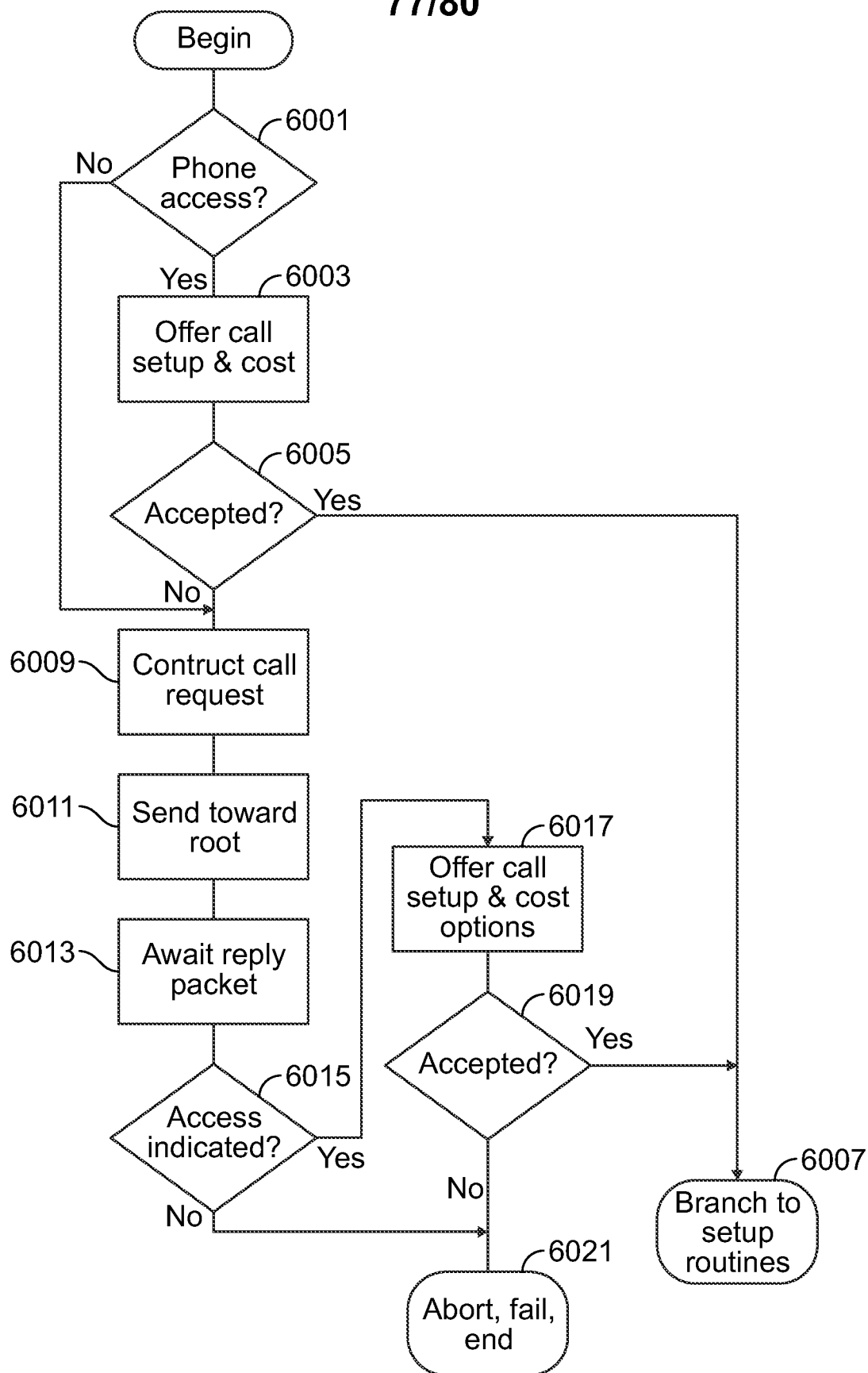


FIG. 60

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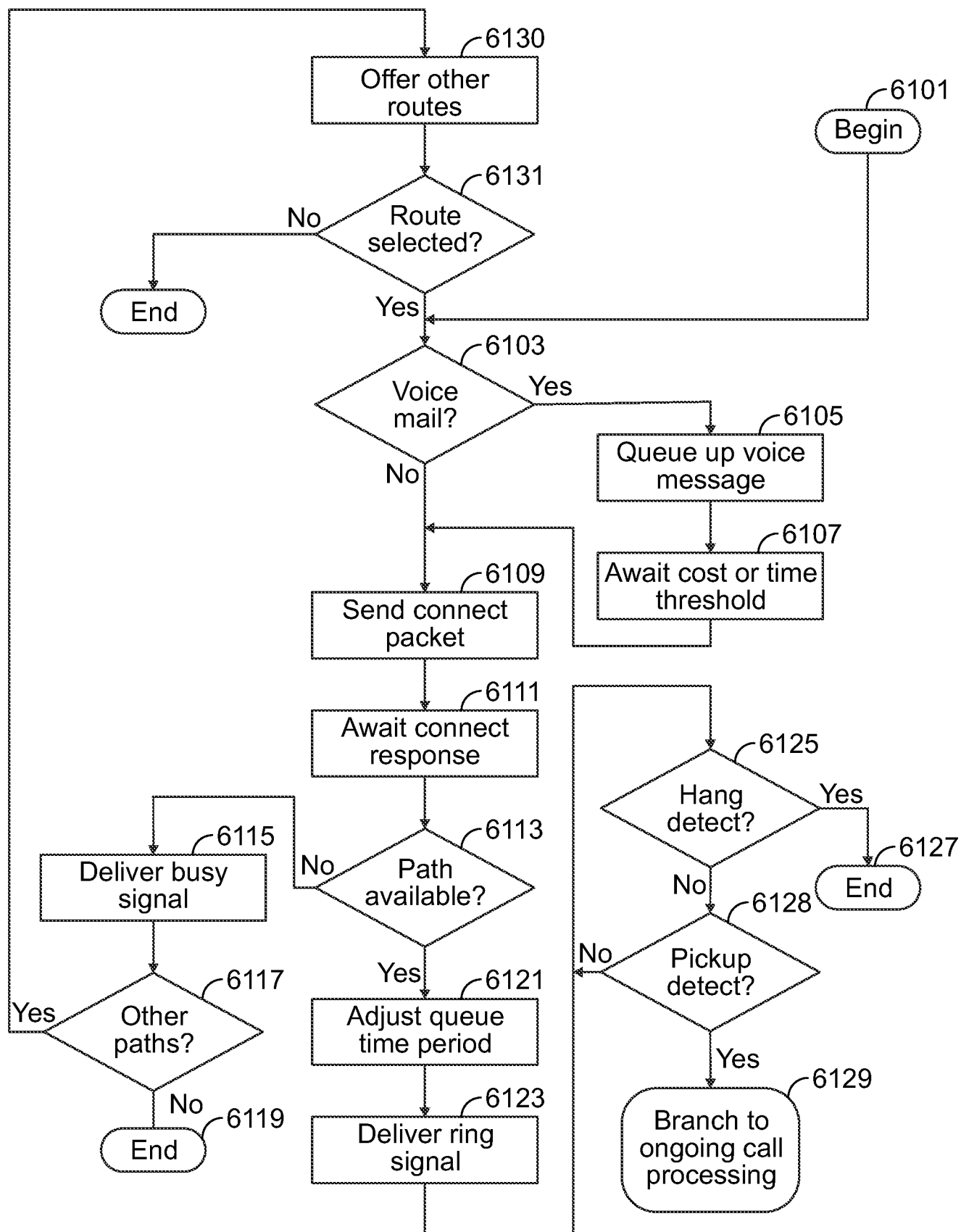


FIG. 61

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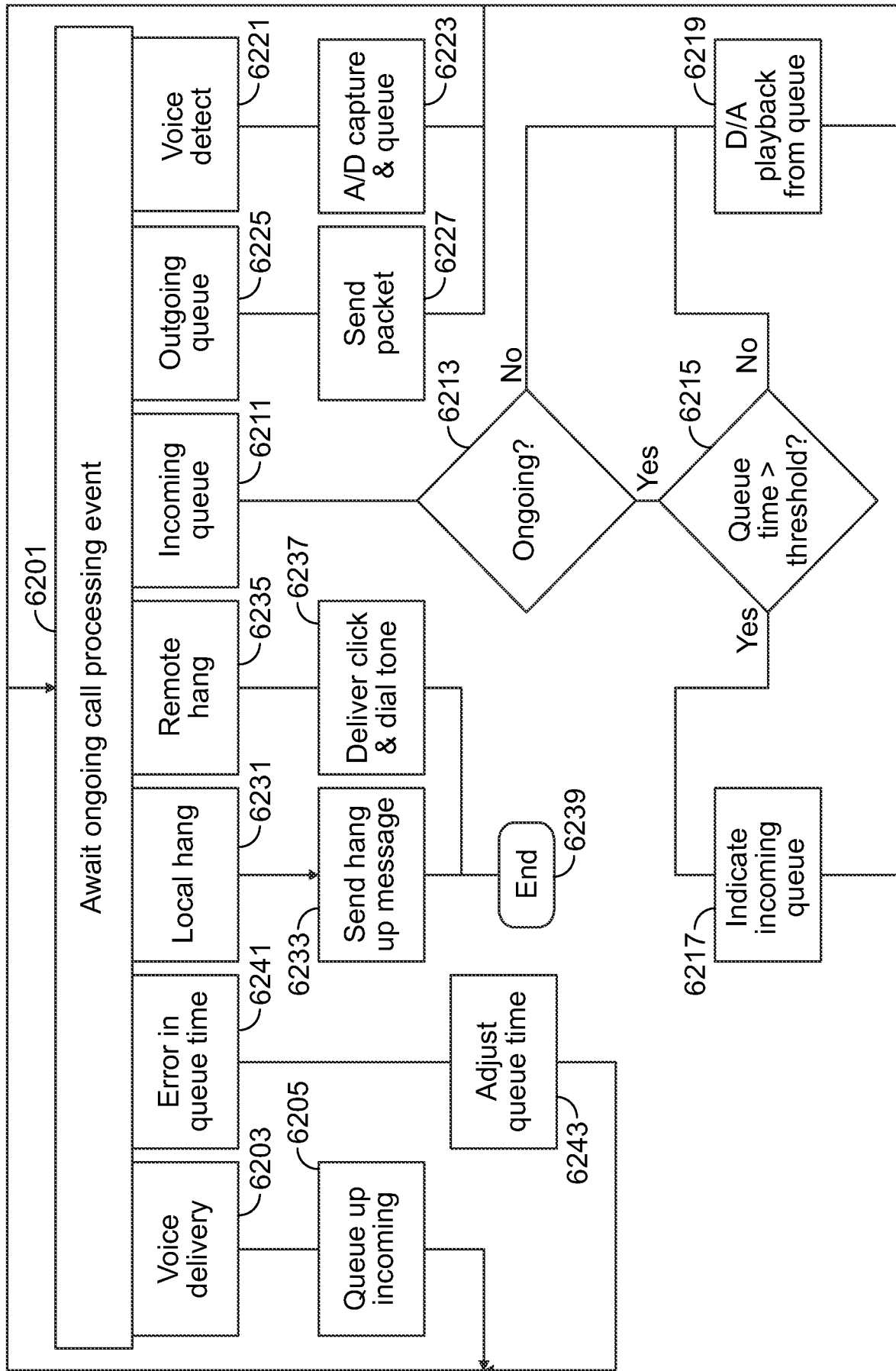


FIG. 62

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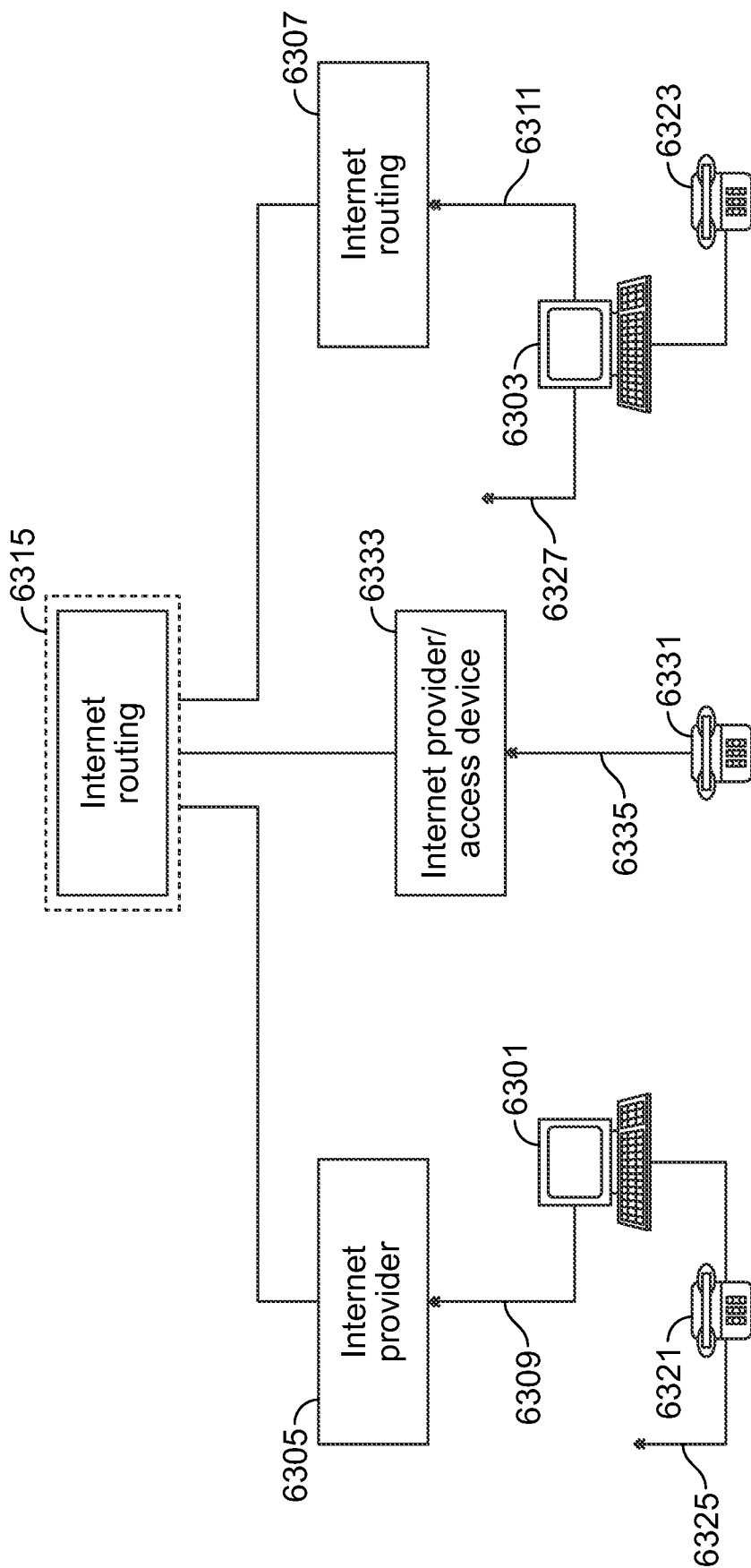


FIG. 63